

HPS-621UTA

High Performance Computing system with single socket
CPU

Quick Reference Guide

2nd Ed –16 February 2023

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Part No. E2017T2S2A1R

FCC Statement



THIS DEVICE COMPLIES WITH PART 15 FCC RULES. OPERATION IS SUBJECT TO THE FOLLOWING TWO CONDITIONS:

(1) THIS DEVICE MAY NOT CAUSE HARMFUL INTERFERENCE.

(2) THIS DEVICE MUST ACCEPT ANY INTERFERENCE RECEIVED INCLUDING INTERFERENCE THAT MAY CAUSE UNDESIRE OPERATION.

THIS EQUIPMENT HAS BEEN TESTED AND FOUND TO COMPLY WITH THE LIMITS FOR A CLASS "A" DIGITAL DEVICE, PURSUANT TO PART 15 OF THE FCC RULES.

THESE LIMITS ARE DESIGNED TO PROVIDE REASONABLE PROTECTION AGAINST HARMFUL INTERFERENCE WHEN THE EQUIPMENT IS OPERATED IN A COMMERCIAL ENVIRONMENT. THIS EQUIPMENT GENERATES, USES, AND CAN RADIATE RADIO FREQUENCY ENERGY AND, IF NOT INSTALLED AND USED IN ACCORDANCE WITH THE INSTRUCTION MANUAL, MAY CAUSE HARMFUL INTERFERENCE TO RADIO COMMUNICATIONS.

OPERATION OF THIS EQUIPMENT IN A RESIDENTIAL AREA IS LIKELY TO CAUSE HARMFUL INTERFERENCE IN WHICH CASE THE USER WILL BE REQUIRED TO CORRECT THE INTERFERENCE AT HIS OWN EXPENSE.

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1. Getting Started

1.1 Safety Precautions

Warning!



Always completely disconnect the power cord from your chassis whenever you work with the hardware. Do not make connections while the power is on. Sensitive electronic components can be damaged by sudden power surges. Only experienced electronics personnel should open the PC chassis.

Caution!



Always ground yourself to remove any static charge before touching the CPU card. Modern electronic devices are very sensitive to static electric charges. As a safety precaution, use a grounding wrist strap at all times. Place all electronic components in a static-dissipative surface or static-shielded bag when they are not in the chassis.

1.2 Packing List

- 1 x HPS-621UTA barebone system
 - HPM-621UA motherboard
 - 1300W PSU
- 2 x front door keys



If any of the above items is damaged or missing, contact your retailer.

1.3 System Specifications

| Component | |
|--------------------------------|--|
| Processor | Single 2nd Gen. Intel® Xeon® Scalable Processors / Intel® Xeon® Scalable Processors up to 150W TDP L10 system: 1 x Intel® Xeon® Gold 6230 Processor CD8069504193701SRF8W, Intel(BCC-CPU-6230R) At CPU |
| Platform Controller Hub | Intel C621 |
| System Memory | 6 x DDR4 2933/2666/2400/2133 RDIMM/LRDIMM up to 768GB L10 system: 2 x DDR4 2666 16GB 288PIN 0~85C M4R0-AGS1BCIK, Innodisk, RDIMM (BCC-MEM-16G-04R) At DIMM3, DIMM6 |
| BIOS Information | AMI UEFI BIOS |
| Watchdog Timer | System reset event 0.1~6553.5 second. (IPMI command) |
| H/W Status Monitor | Temperature. Fan. Voltage. Case open. (1 x 2.5mm pitch Box Wafer, Pinrex 753-71-02TW07 or equivalent) Please refer to note 1 for more information. |
| RAID | Intel C621 RAID 0,1,5 |
| TPM | TPM 2.0 onboard |
| BMC | IPMI 2.0 with AST 2500 BMC controller onboard. |
| Other | 1 x Inlet sensor board 1 x Outlet sensor board 1 x Case open sensor |
| Expansion | |
| PCIe (Gen X, Lanes) | 3x PCIe x16 slots, 3 x PCIe x8 slots Slot 1, NA (This is for PCI 3.0 slot) Slot 2, PCIe 3.0 x8 Slot 3, PCIe 3.0 x16 (Display GPU – P2200 for L10 system) Slot 4, PCIe 3.0 x8 Slot 5, PCIe 3.0 x16 (Display GPU – RTX5000 for L10 system) Slot 6, PCIe 3.0 x8 Slot 7, PCIe 3.0 x16 (Slot 7 is the slot closest to CPU) |
| PCI | 1 x PCI 3.0 slot Slot 1, PCI 3.0 |
| Storage | |

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| | |
|-------------------------------------|---|
| M.2 (Key-X, Size, Signal) | 1 x M.2 M-Key PCIe 3.0 x4 NVMe SSD 2242/2260/2280 form factor |
| 2.5" Drive Bay (Height) | 3 x 2.5" Drive Bay L10 system: 1 x 2.5" SATA3 SSD 240GB TLC 0~70C (non-IPS) TS240GSSD452K-PHX1, Transcend, 1.02 DWPD (BCC-2S3S-240G-03R) |
| Edge I/O (Front) | |
| USB Port | 2 x USB 3.2 Gen1 Ports |
| Power Button | 1 power button |
| Reset Button | 1 reset button |
| LED Indicator | 1 x Power state 1 x Disk drive activity 1 x Network activity(LAN1) |
| Edge I/O (Rear) | |
| USB Port | 4 x USB 3.2 Gen1 Ports |
| COM Port | 1 x RS232 (Bracket shared with VGA port, RS232 on the top) At Slot 0 |
| VGA | Display Priority: VGA 1 x VGA (Bracket shared with Serial port, VGA on the bottom) At Slot 0 |
| RJ-45 | 4 x RJ-45 (LAN 1 port shared with IPMI 2.0) |
| Display | |
| Graphic Chipset | AST2500 BMC controller |
| Resolution | 1920 x 1200@60Hz 32bpp |
| Ethernet | |
| LAN Chipset | 4 x Intel I210AT |
| Specification | Gigabit Ethernet Controller |
| LED Indicator | Follow Avalue standard. |
| Power Requirement | |
| ACPI | Yes |
| Power Mode | H/W: ATX power well design only BMC: AT (Default) |
| Power Supply Unit | Delta 1300W PSU |
| Mechanical & Environment | |
| Operating Temp. | Condition 1: Temperature: 0 to 40 degree C (L6) Condition 2: Temperature: 0 to 35 degree C (L10, GPU RTX5000+P2200) Condition 3: Temperature: 0 to TBC degree (L10 system, depends on added card spec.) |

| | |
|---------------------------|---|
| Storage Temp. | -40°C 24hrs IEC60068-2-1 Cold test Test : Ab 70°C/ RH95% 24hrs IEC 60068-2-3 Test:Ca |
| Operating Humidity | 40°C/RH95%/24hrs IEC 60068-2-56 Test:Cb |
| Dimension (W*L*H) | 340mm(W) x 488mm(H) x 528mm(D) with external handles & pedestal stand. |
| Weight | 19.2 kg |
| Vibration Test | <p>Operational :</p> <ol style="list-style-type: none"> 0.25 Grms Random Operation mode Test Frequency : 5-500Hz Test Axis : X,Y and Z axis 30 min. per each axis IEC 60068-2-64 Test:Fh <p>Non-operational :</p> <ol style="list-style-type: none"> Test Acceleration : 0.5G Test frequency : 5~500 Hz Sweep : 1 Oct/ per one minute. (logarithmic) Test Axis : X,Y and Z axis Test time :30 min. each axis System condition : Non-Operating mode Reference IEC 60068-2-6 Testing procedures <p>Package Vibration Test:</p> <ol style="list-style-type: none"> PSD: 0.026G²/Hz , 2.16 Grms Non-operation mode Test Frequency : 5-500Hz Test Axis : X,Y and Z axis 30 min. per each axis IEC 60068-2-64 Test:Fh |
| Shock Test | <p>Operational :</p> <ol style="list-style-type: none"> Wave form : Half Sine wave Acceleration Rate : 5.0G for operation mode Duration Time : 11ms No. of Shock : Z axis 300 times Test Axis: Z axis Operation mode Reference IEC 60068-2-27 Testing procedures |
| Drop Test | <p>Package drop test :</p> <ol style="list-style-type: none"> One corner, three edges, six face ISTA 2A, IEC-60068-2-32 Test:Ed |

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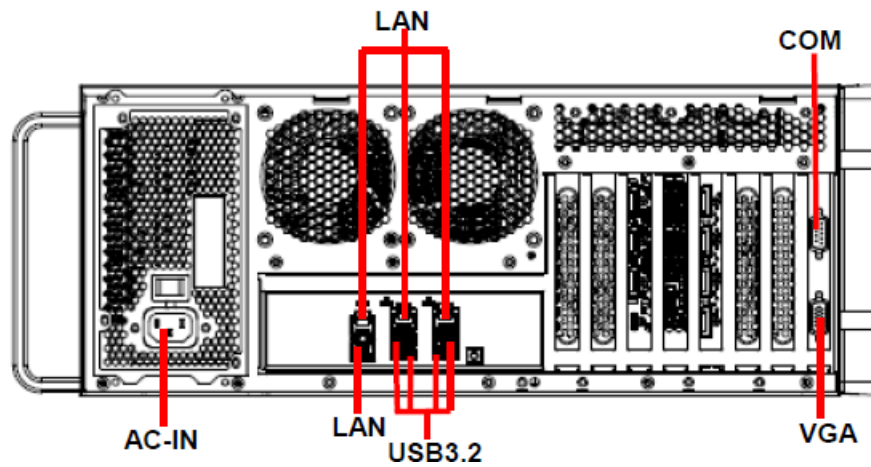
| Software Support | |
|-----------------------|---|
| OS Information | Windows : Windows 10 IOT Enterprise, Windows server 2016, Windows server 2019 Linux : Ubuntu 16.04 and 18.04 |
| Certification | |
| EMI Test | CE/FCC Class A EMC Safety Requirement: ESD +8KV Contact and +15KV Air |
| Safety | EN 62368 |
| In-Box Accessory | |
| Accessory | Front door key. |



Note: Specifications are subject to change without notice.

1.4 System Overview

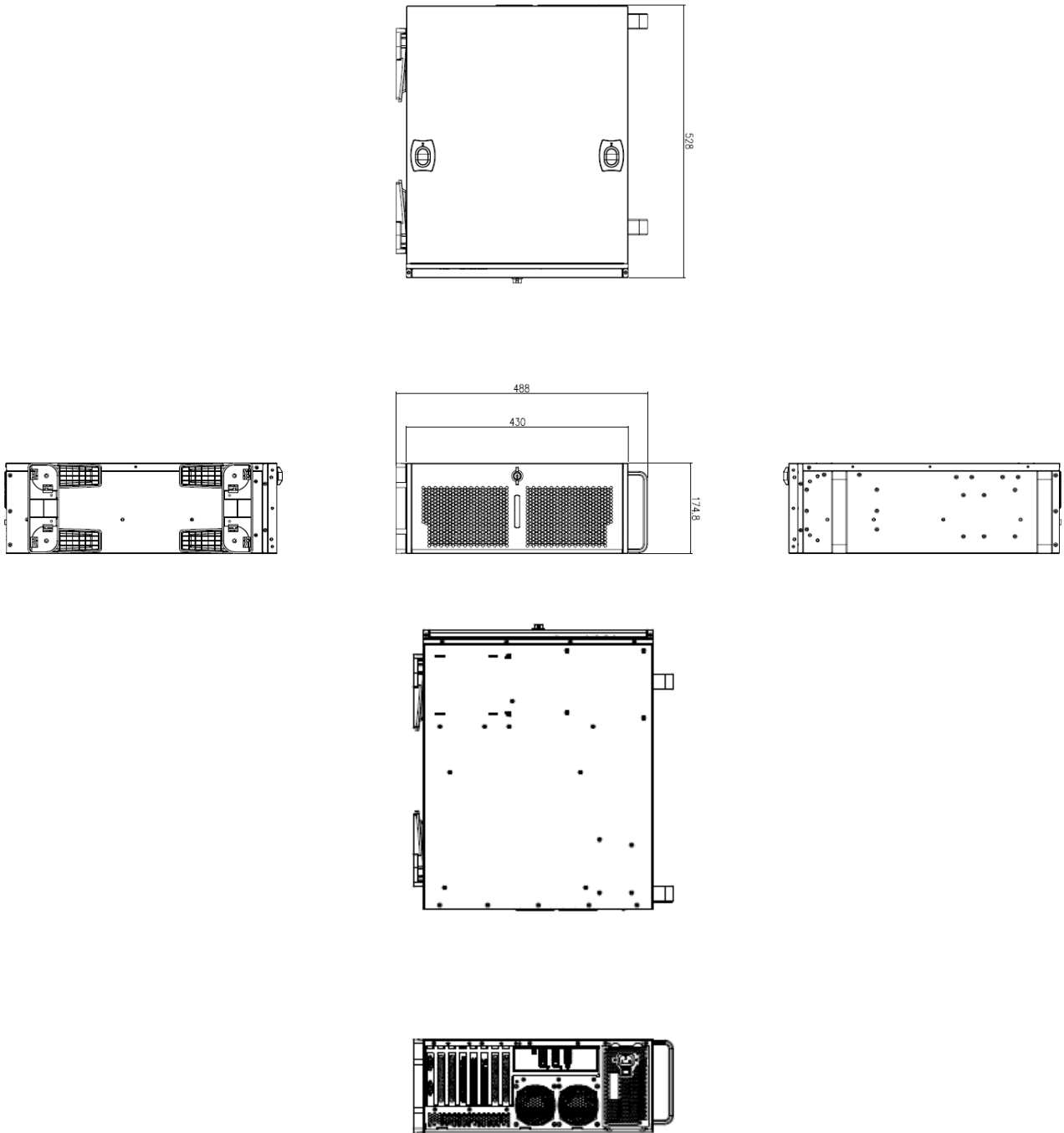
1.4.1 Front View



Connectors

| Label | Function | Note |
|--------|------------------------------|-------------------|
| COM | Serial port connector | D-sub 9-pin, male |
| LAN | 4 x RJ-45 Ethernet connector | |
| USB3.2 | 4 x USB3.2 connector | |
| VGA | VGA connector | |
| AC-IN | AC power-in connector | |

1.5 System Dimensions



(Unit: mm)

2. Hardware Configuration

Jumper and Connector Setting

For advanced information, please refer to:

- 1- HPM-621UA included in this manual.

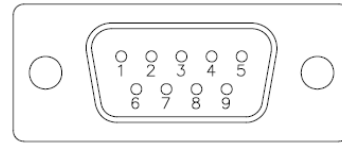
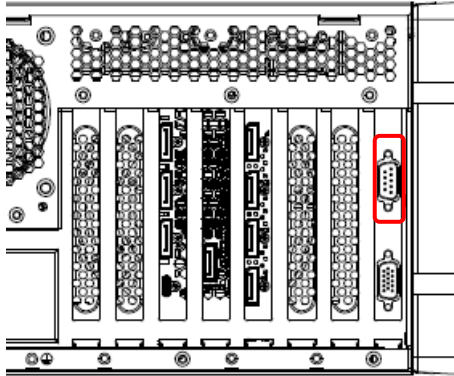


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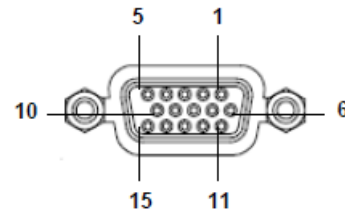
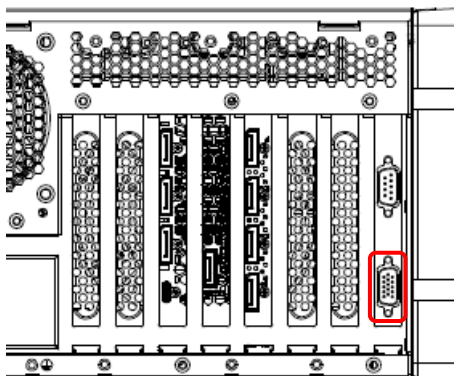
2.1 HPS-621UTA connector mapping

2.1.1 Serial Port connector (COM)



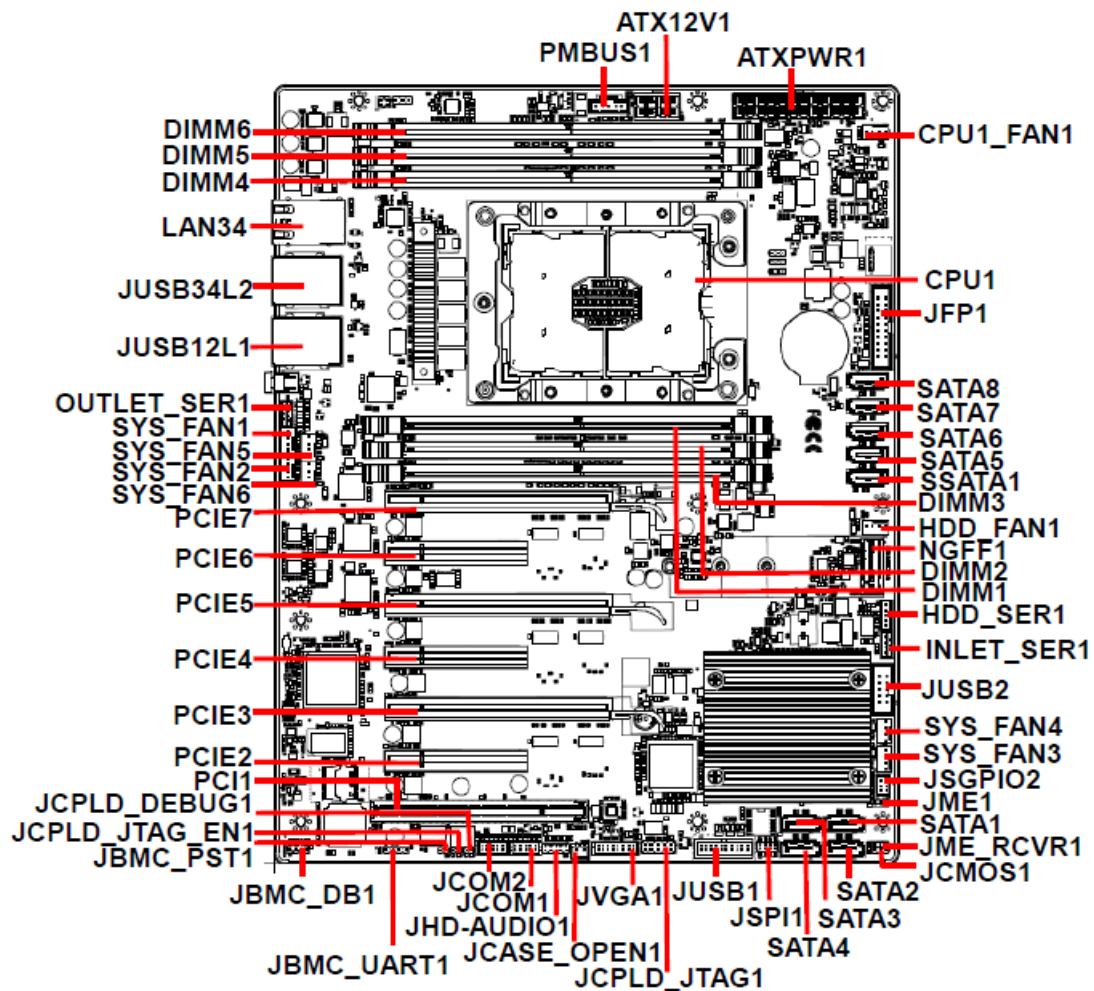
| Signal | PIN | PIN | Signal |
|--------|-----|-----|--------|
| DCD# | 1 | 6 | DSR# |
| RXD | 2 | 7 | RTS# |
| TXD | 3 | 8 | CTS# |
| DTR# | 4 | 9 | RI# |
| GND | 5 | | |

2.1.2 VGA connector (VGA)



| PIN | Signal | PIN | Signal | PIN | Signal |
|-----|--------|-----|--------|-----|--------|
| 1 | RED | 6 | GND | 11 | NC |
| 2 | GREEN | 7 | GND | 12 | DDCDAT |
| 3 | BLUE | 8 | GND | 13 | HSYNC |
| 4 | NC | 9 | +5V | 14 | VSYS |
| 5 | GND | 10 | GND | 15 | DDCCLK |

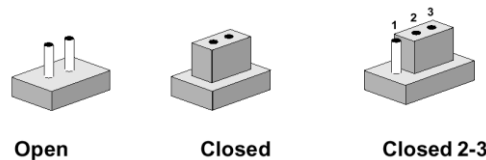
2.2 HPM-621UA Product Overview



2.3 HPM-621UA Jumper and Connector List

You can configure your board to match the needs of your application by setting jumpers. A jumper is the simplest kind of electric switch.

It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To “close” a jumper you connect the pins with the clip. To “open” a jumper you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2, and 3. In this case, you would connect either two pins.



The jumper settings are schematically depicted in this manual as follows:



A pair of needle-nose pliers may be helpful when working with jumpers.

Connectors on the board are linked to external devices such as hard disk drives, a keyboard, or floppy drives. In addition, the board has a number of jumpers that allow you to configure your system to suit your application.

If you have any doubts about the best hardware configuration for your application, contact your local distributor or sales representative before you make any changes.

The following tables list the function of each of the board’s jumpers and connectors.

Jumpers

| Label | Function | Note |
|----------------|--|----------------------------|
| JME_RCVR1 | ME Firmware Recovery | 3 x 1 header, pitch 2.00mm |
| JME1 | Flash Descriptor Security override | 3 x 1 header, pitch 2.00mm |
| JBMC_DB1 | BMC strap setting | 4 x 2 header, pitch 2.00mm |
| JCMOS1 | Clear CMOS | 3 x 1 header, pitch 2.00mm |
| JBMC_PST1 | CPLD strap setting for BMC Present or not | 2 x 1 header, pitch 2.00mm |
| JCPLD_JTAG_EN1 | CPLD JTAG setting | 2 x 1 header, pitch 2.00mm |
| JCPLD_DEBUG1 | CPLD DEBUG header | 2 x 1 header, pitch 2.00mm |

Connectors

| Label | Function | Note |
|----------|------------------------|---------------------------|
| SYS_FAN1 | System fan connector 1 | 4 x 1 wafer, pitch 2.54mm |

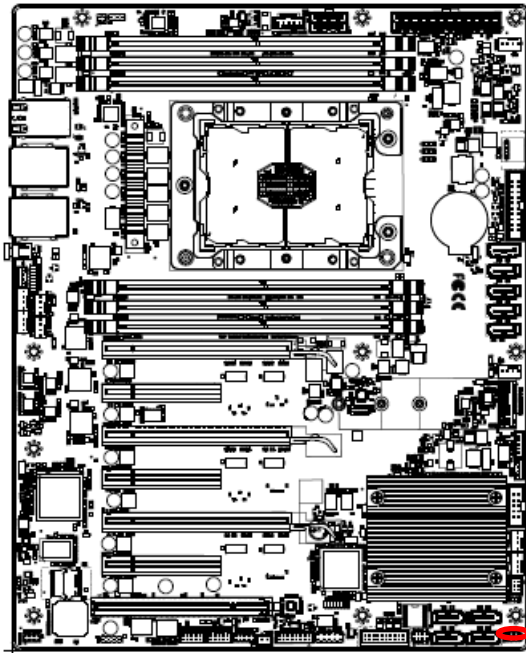
| | | |
|--------------------|--|----------------------------|
| SYS_FAN2 | System fan connector 2 | 4 x 1 wafer, pitch 2.54mm |
| SYS_FAN3 | System fan connector 3 | 4 x 1 wafer, pitch 2.54mm |
| SYS_FAN4 | System fan connector 4 | 4 x 1 wafer, pitch 2.54mm |
| SYS_FAN5 | System fan connector 5 | 4 x 1 wafer, pitch 2.54mm |
| SYS_FAN6 | System fan connector 6 | 4 x 1 wafer, pitch 2.54mm |
| CPU1_FAN1 | CPU fan connector | 4 x 1 wafer, pitch 2.54mm |
| HDD_FAN1 | HDD fan connector | 4 x 1 wafer, pitch 2.54mm |
| JCOM1 | Serial port 1 connector | 5 x 2 wafer, pitch 2.00mm |
| JCOM2 | Serial port 2 connector | 5 x 2 wafer, pitch 2.00mm |
| JSGPIO2 | Serial General Purpose I/O connector 2 | 3 x 2 wafer, pitch 2.00mm |
| PCIE2 | PCIe 3.0 x8 | |
| PCIE3 | PCIe 3.0 x16 | |
| PCIE4 | PCIe 3.0 x8 | |
| PCIE5 | PCIe 3.0 x16 | |
| PCIE6 | PCIe 3.0 x8 | |
| PCIE7 | PCIe 3.0 x16 (Slot 7 is the slot closest to CPU) | |
| PCI1 | PCI 3.0 connector | |
| JFP1 | Front Panel connector | 10 x 2 wafer, pitch 2.54mm |
| JUSB12L1 | 2 x USB3.2 Gen1 connector 1 x RJ-45 Ethernet (LAN1 Share IPMI Port) | |
| JUSB34L2 | 2 x USB3.2 Gen1 connector 1 x RJ-45 Ethernet | |
| LAN34 | 2 x RJ-45 Ethernet | |
| JUSB1 | USB3.2 Gen1 connector | 10 x 2 wafer, pitch 2.00mm |
| JUSB2 | USB2.0 connector | 5 x 2 wafer, pitch 2.54mm |
| JHD-AUDIO1 | Audio connector | 5 x 2 header, pitch 2.00mm |
| JSPI1 | SPI connector | 4 x 2 header, pitch 2.00mm |
| SATA1-8 | 8 x Serial ATA connector | |
| SSATA1 | Second Serial ATA connector | |
| DIMM1-6 | 6 x DDR4 DIMM socket | |
| JBMC_UART1 | For BMC debug message read | 4 x 1 header, pitch 2.54mm |
| JCASE_OPEN1 | CASE OPEN connector | 2 x 1 wafer, pitch 2.50mm |
| ATX12V1 | ATX 12V power connector | 4 x 2 wafer, pitch 4.20mm |
| ATXPWR1 | ATX power connector | 12 x 2 wafer, pitch 4.20mm |

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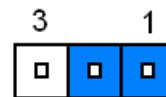
| | | |
|--------------------|---|----------------------------|
| PMBUS1 | Power supply PMBus connector | 5 x 1 wafer, pitch 2.54mm |
| INLET_SER1 | Inlet Thermal Sensors connector | 4 x 1 wafer, pitch 2.00mm |
| OUTLET_SER1 | Outlet Thermal Sensors connector | 4 x 1 wafer, pitch 2.00mm |
| HDD_SER1 | HDD Backplane thermal Sensors connector | 5 x 1 wafer, pitch 2.00mm |
| NGFF1 | M.2 M-Key PCIe 3.0 x4 NVMe SSD | |
| CPU1 | CPU1 socket | |
| JVGA1 | VGA connector | 8 x 2 wafer, pitch 2.00mm |
| JCPLD_JTAG1 | CPLD JTAG header | 5 x 2 header, pitch 2.54mm |

2.4 HPM-621UA Setting Jumpers & Connectors

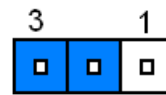
2.4.1 ME Firmware Recovery (JME_RCVR1)



Normal*

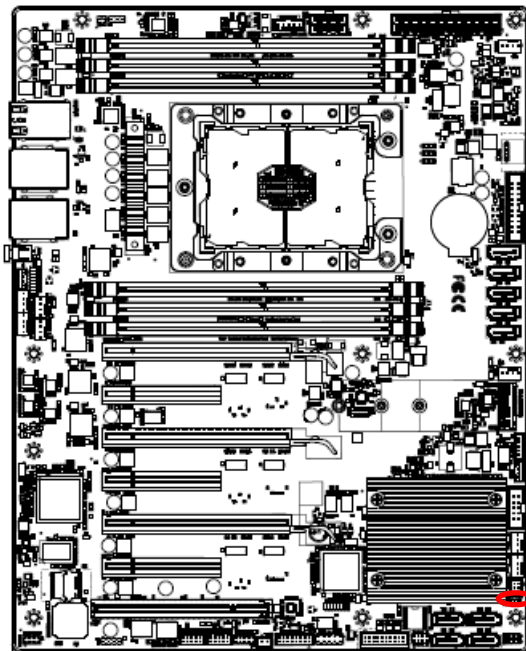


ME FORCE UPDATE

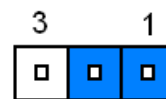


* Default

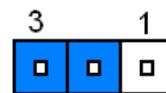
2.4.2 Flash Descriptor Security override (JME1)



Override disable*

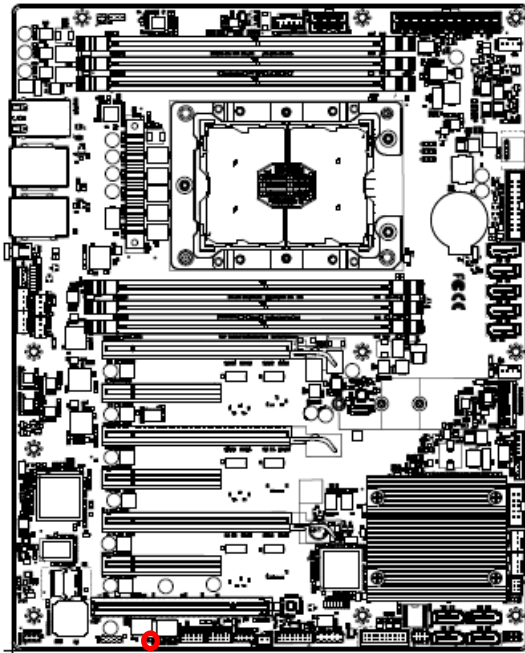


Override enable



* Default

2.4.5 CPLD strap setting for BMC Present or not (JBMC_PST1)

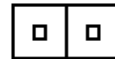


BMC Present*



1

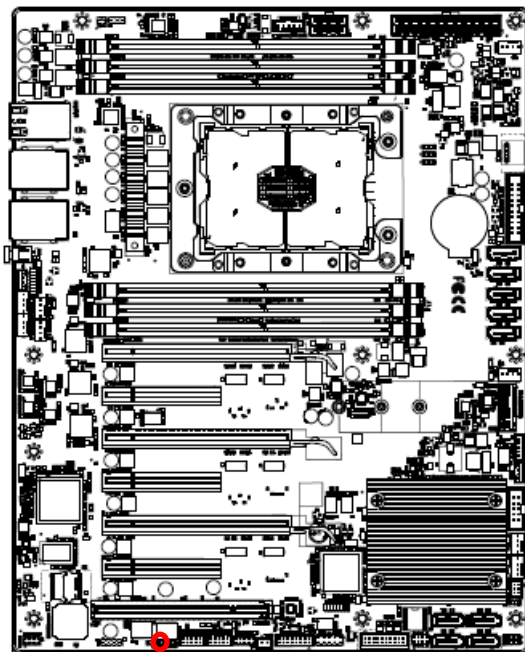
Non BMC



1

* Default

2.4.6 CPLD JTAG setting (JCPLD_JTAG_EN1)

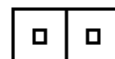


CPLD JTAG as JTAG*



1

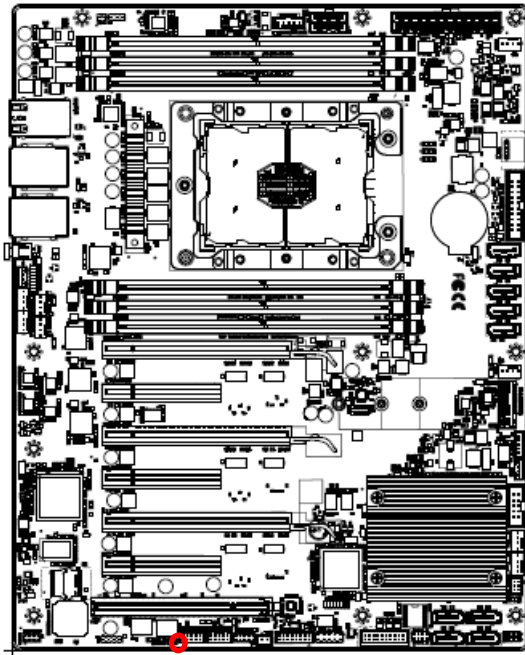
CPLD JTAG as GPIO



1

* Default

2.4.7 CPLD DEBUG header (JCPLD_DEBUG1)

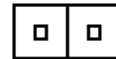


Force Power on



1

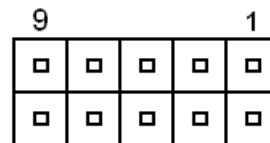
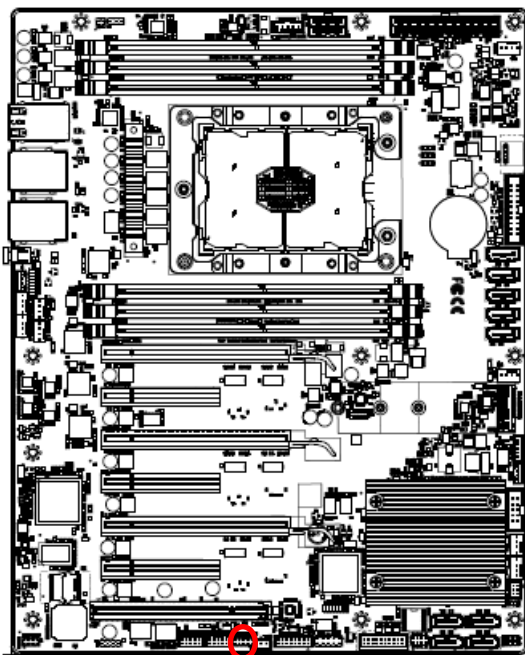
Normal mode*



1

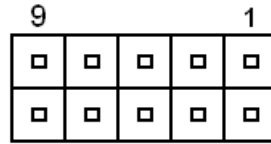
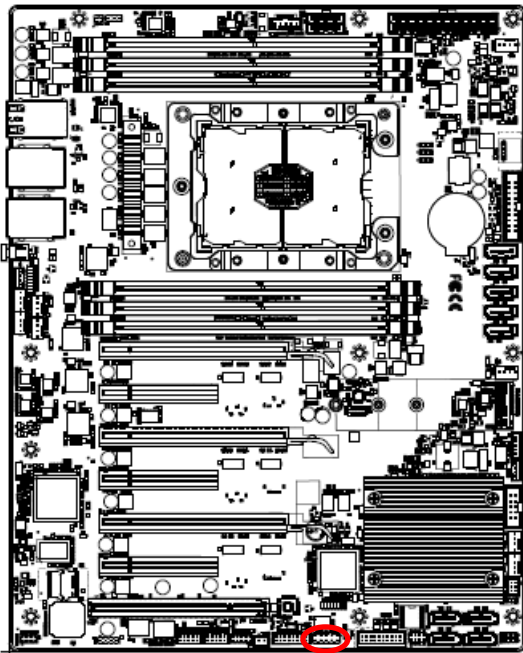
* Default

2.4.8 Audio connector (JHD-AUDIO1)



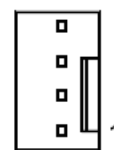
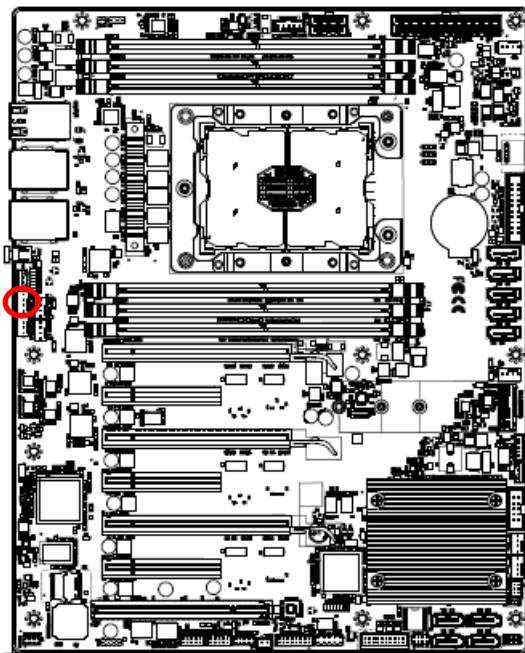
| Signal | PIN | PIN | Signal |
|----------------|-----|-----|-----------------|
| +3.3V | 1 | 2 | GND |
| AUD_AZA_SYNC_R | 3 | 4 | AUD_AZA_BCLK_R |
| AUD_AZA_SDO_R | 5 | 6 | AUD_AZA_SDI0 |
| AUD_AZA_SDI1 | 7 | 8 | AUD_AZA_RST_R_N |
| +5VSB | 9 | 10 | GND |

2.4.9 CPLD JTAG header (JCPLD_JTAG1)



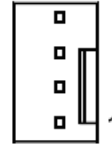
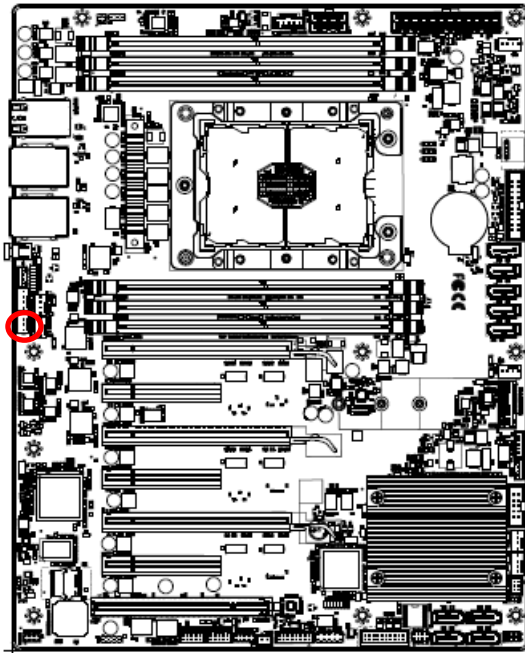
| Signal | PIN | PIN | Signal |
|---------------|-----|-----|-------------------|
| JTAG_TCK_CONN | 1 | 2 | CPLD_JTAG_MUX_CTL |
| JTAG_TDO_CONN | 3 | 4 | +3.3VSB |
| JTAG_TMS_CONN | 5 | 6 | NC |
| NC | 7 | 8 | NC |
| JTAG_TDI_CONN | 9 | 10 | GND |

2.4.10 System fan connector 1 (SYS_FAN1)



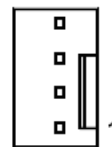
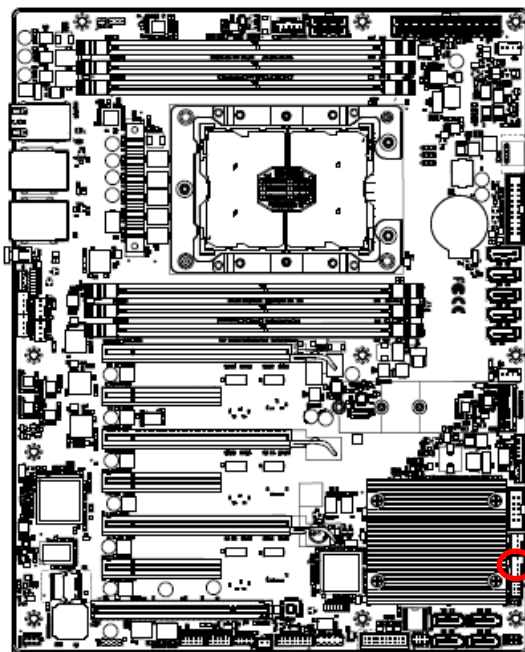
| Signal | PIN |
|-----------|-----|
| SYS_PWM1 | 4 |
| FAN_TACH2 | 3 |
| +12V | 2 |
| GND | 1 |

2.4.11 System fan connector 2 (SYS_FAN2)



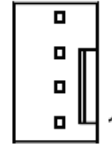
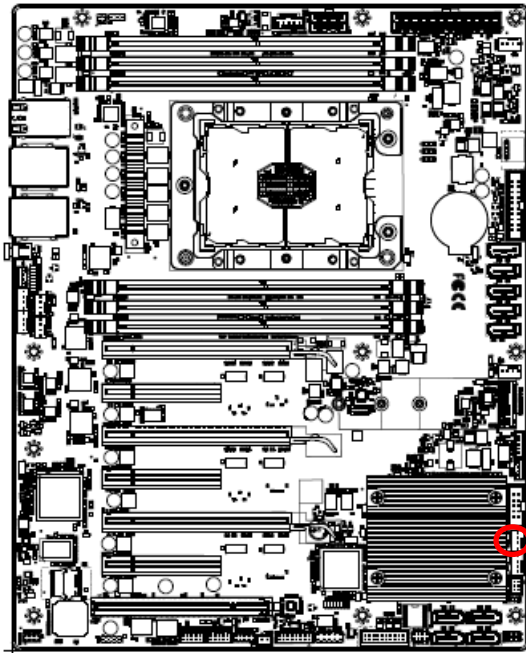
| Signal | PIN |
|-----------|-----|
| SYS_PWM2 | 4 |
| FAN_TACH3 | 3 |
| +12V | 2 |
| GND | 1 |

2.4.12 System fan connector 3 (SYS_FAN3)



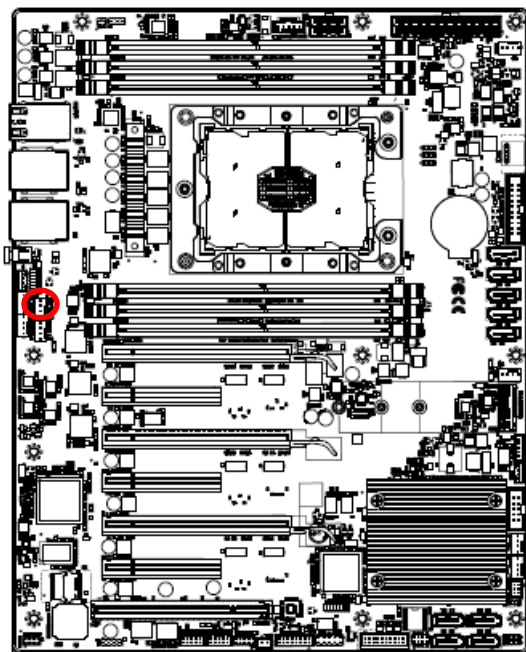
| Signal | PIN |
|-----------|-----|
| SYS_PWM3 | 4 |
| FAN_TACH4 | 3 |
| +12V | 2 |
| GND | 1 |

2.4.13 System fan connector 4 (SYS_FAN4)



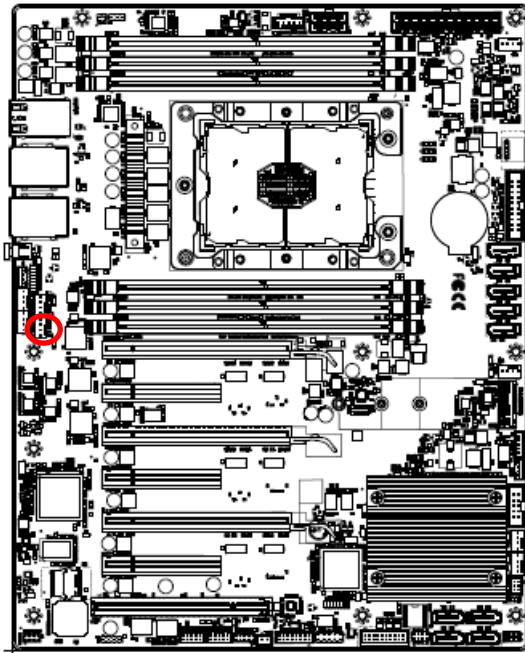
| Signal | PIN |
|-----------|-----|
| SYS_PWM4 | 4 |
| FAN_TACH6 | 3 |
| +12V | 2 |
| GND | 1 |

2.4.14 System fan connector 5 (SYS_FAN5)



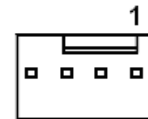
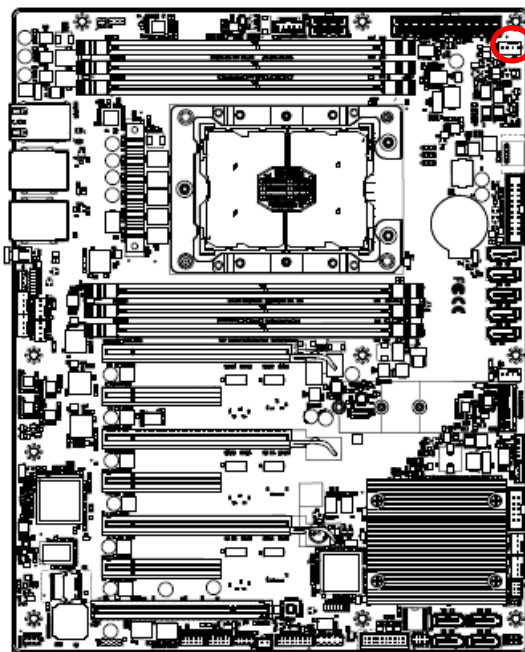
| Signal | PIN |
|-----------|-----|
| SYS_PWM5 | 4 |
| FAN_TACH1 | 3 |
| +12V | 2 |
| GND | 1 |

2.4.15 System fan connector 6 (SYS_FAN6)



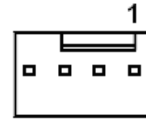
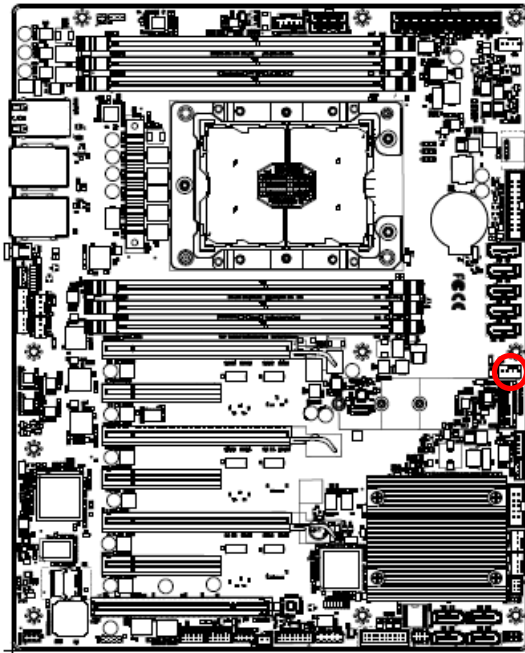
| Signal | PIN |
|-----------|-----|
| SYS_PWM6 | 4 |
| FAN_TACH7 | 3 |
| +12V | 2 |
| GND | 1 |

2.4.16 CPU fan connector (CPU1_FAN1)



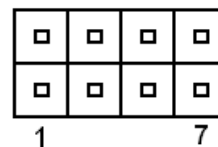
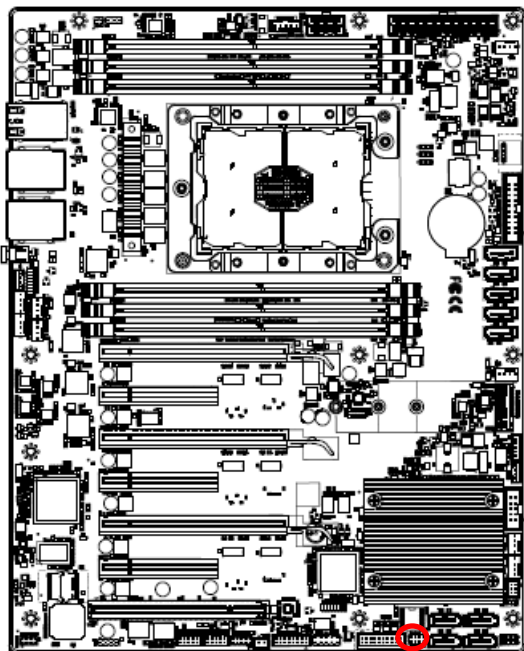
| Signal | PIN |
|-----------|-----|
| GND | 1 |
| +12V | 2 |
| FAN_TACH0 | 3 |
| CPU0_PWM0 | 4 |

2.4.17 HDD fan connector (HDD_FAN1)



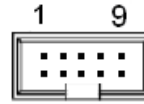
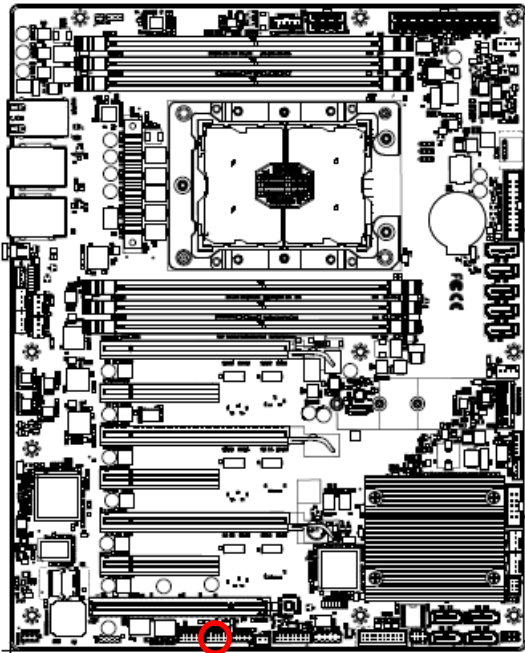
| Signal | PIN |
|-----------|-----|
| GND | 1 |
| +12V | 2 |
| FAN_TACH5 | 3 |
| HDD_PWM1 | 4 |

2.4.18 SPI connector (JSPI1)



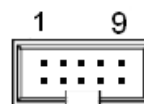
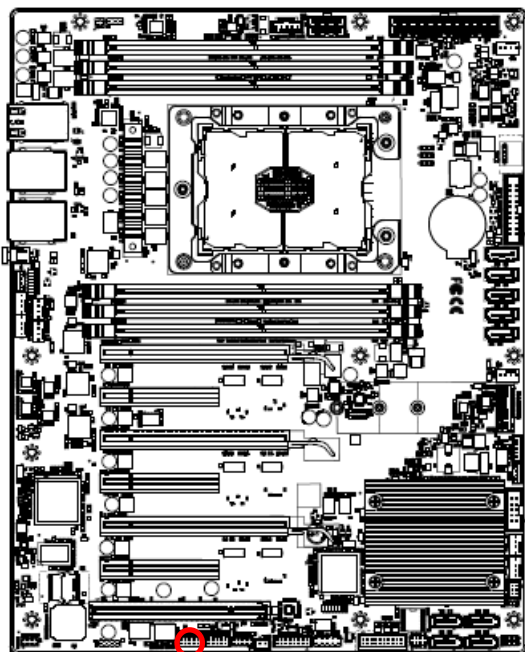
| Signal | PIN | PIN | Signal |
|---------------------|-----|-----|--------------------|
| +3.3VSB | 1 | 2 | GND |
| SPI_BIOS_CS0_N_R | 3 | 4 | SPI_BIOS_FLASH_CLK |
| SPI_BIOS_MISO_FLASH | 5 | 6 | SPI_BIOS_CS0_N_R |
| SPI_PCH_FLASH_IO3 | 7 | 8 | SPI_PCH_FLASH_IO2 |

2.4.19 Serial port 1 connector (JCOM1)



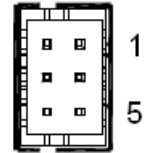
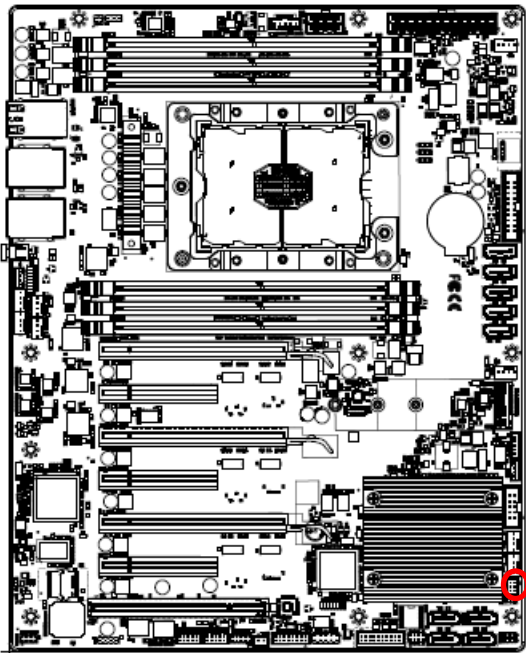
| Signal | PIN | PIN | Signal |
|-----------|-----|-----|-----------|
| COM_DCD#1 | 1 | 2 | COM_RXD1 |
| COM_TXD1 | 3 | 4 | COM_DTR#1 |
| GND | 5 | 6 | COM_DSR#1 |
| COM_RTS#1 | 7 | 8 | COM_CTS#1 |
| COM_RI#1 | 9 | 10 | NC |

2.4.20 Serial port 2 connector (JCOM2)



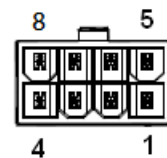
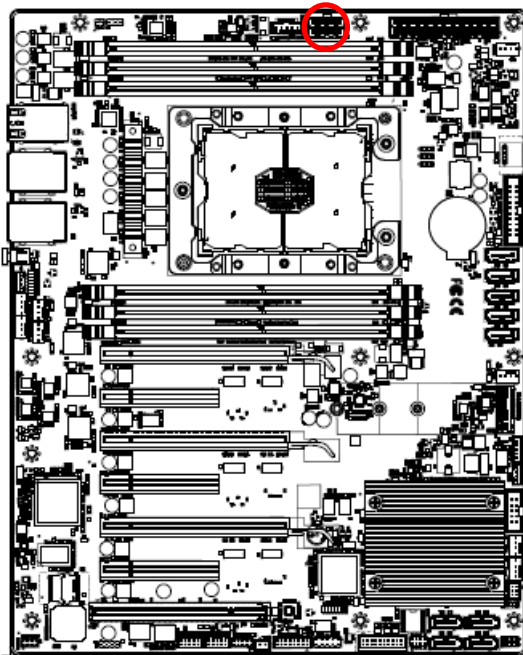
| Signal | PIN | PIN | Signal |
|-----------|-----|-----|-----------|
| COM_DCD#2 | 1 | 2 | COM_RXD2 |
| COM_TXD2 | 3 | 4 | COM_DTR#2 |
| GND | 5 | 6 | COM_DSR#2 |
| COM_RTS#2 | 7 | 8 | COM_CTS#2 |
| COM_RI#2 | 9 | 10 | NC |

2.4.21 Serial General Purpose I/O connector (JSGPIO2)



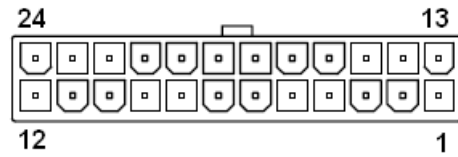
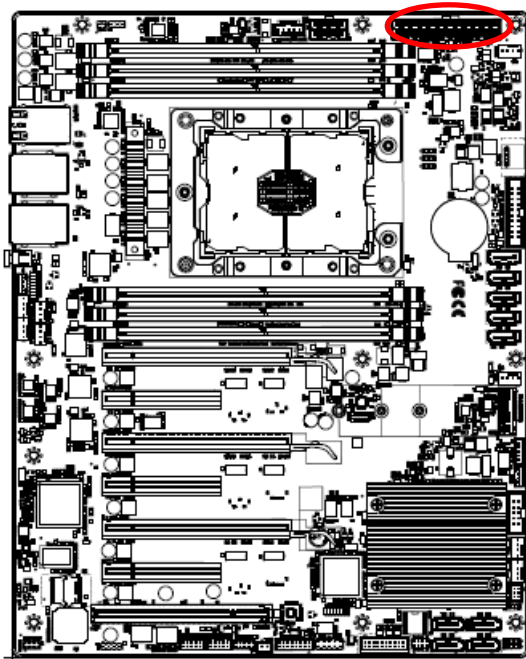
| Signal | PIN | PIN | Signal |
|--------------------|-----|-----|--------------------|
| GND | 2 | 1 | GND |
| SGPIO_SATA_DATA0_R | 4 | 3 | SGPIO_SATA_LOAD_R |
| SGPIO_SATA_DATA1_R | 6 | 5 | SGPIO_SATA_CLOCK_R |

2.4.22 ATX 12V power connector (ATX12V1)



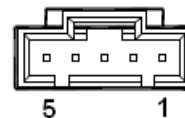
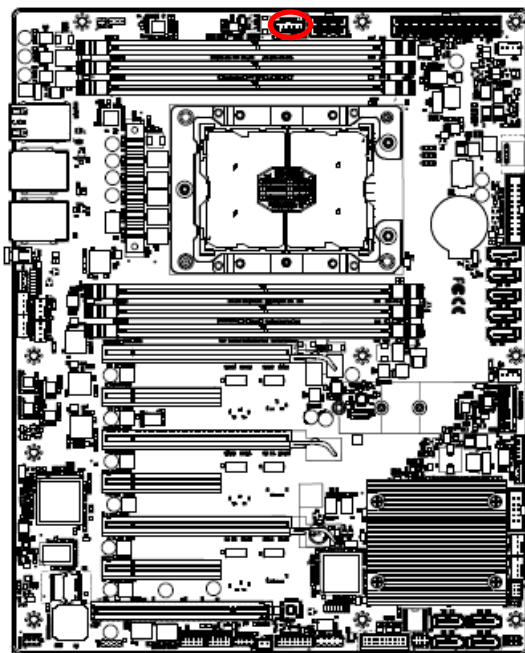
| Signal | PIN | PIN | Signal |
|--------|-----|-----|--------|
| GND | 1 | 5 | +12V |
| GND | 2 | 6 | +12V |
| GND | 3 | 7 | +12V |
| GND | 4 | 8 | +12V |

2.4.23 ATX power connector (ATXPWR1)



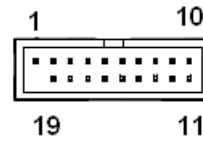
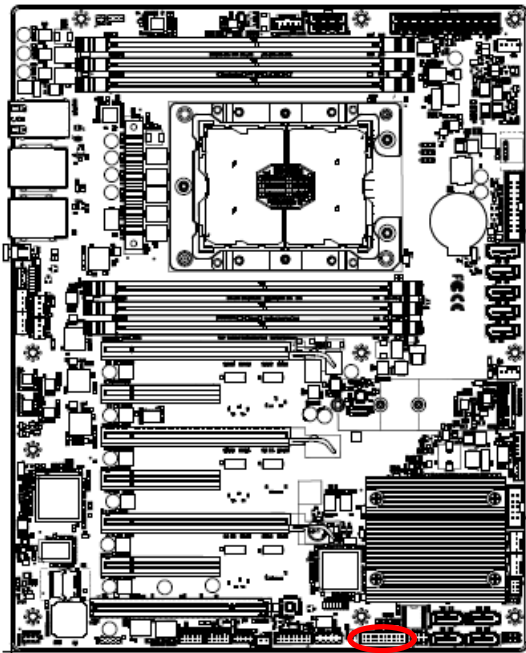
| Signal | PIN | PIN | Signal |
|------------------|-----|-----|----------------|
| +3.3V | 1 | 13 | +3.3V |
| +3.3V | 2 | 14 | -12V |
| GND | 3 | 15 | GND |
| +5V | 4 | 16 | FM_PS_EN_PSU_N |
| GND | 5 | 17 | GND |
| +5V | 6 | 18 | GND |
| GND | 7 | 19 | GND |
| PWRGD_PS_PWROK_R | 8 | 20 | NC |
| +V5SB | 9 | 21 | +5V |
| +12V | 10 | 22 | +5V |
| +12V | 11 | 23 | +5V |
| +3.3V | 12 | 24 | GND |

2.4.24 Power supply PMBus connector (PMBUS1)



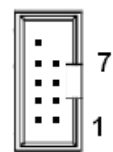
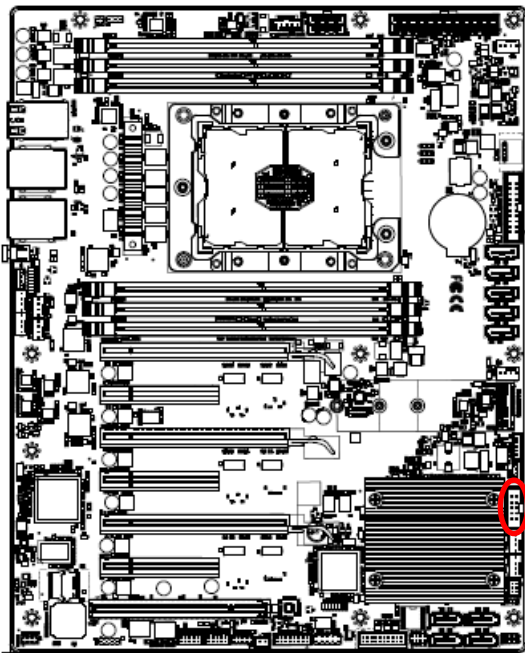
| Signal | PIN |
|----------------|-----|
| PSU_z_SCL | 1 |
| PSU_z_SDA | 2 |
| PSU1_ALERT_z_N | 3 |
| GND | 4 |
| NC | 5 |

2.4.25 USB3.2 Gen1 connector (JUSB1)



| Signal | PIN | PIN | Signal |
|------------|-----|-----|------------|
| | | 1 | +5VSB |
| +5VSB | 19 | 2 | USB3_z_RN5 |
| USB3_z_RN6 | 18 | 3 | USB3_z_RP5 |
| USB3_z_RP6 | 17 | 4 | GND |
| GND | 16 | 5 | USB3_z_TN5 |
| USB3_z_TN6 | 15 | 6 | USB3_z_TP5 |
| USB3_z_TP6 | 14 | 7 | GND |
| GND | 13 | 8 | USB3_z_PN5 |
| USB3_z_PN6 | 12 | 9 | USB3_z_PP5 |
| USB3_z_PP6 | 11 | 10 | USB_a_OC2# |

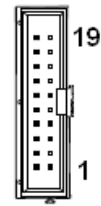
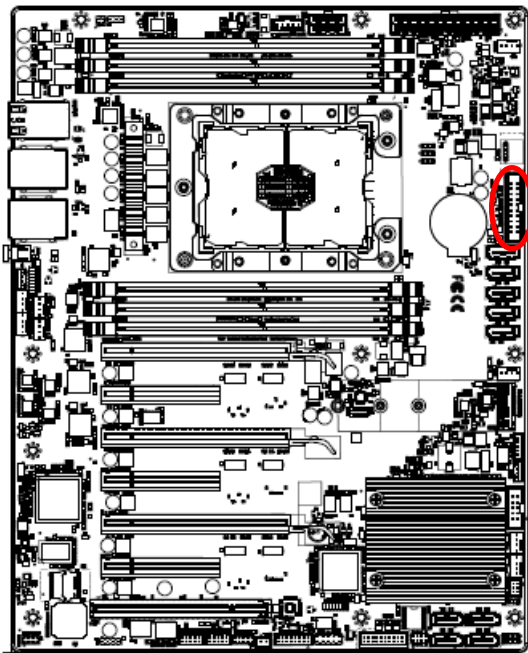
2.4.26 USB2.0 connector (JUSB2)



| Signal | PIN | PIN | Signal |
|-----------|-----|-----|-----------|
| NC | 10 | | |
| GND | 8 | 7 | GND |
| USB_z_PP8 | 6 | 5 | USB_z_PP7 |
| USB_z_PN8 | 4 | 3 | USB_z_PN7 |
| +5VSB | 2 | 1 | +5VSB |

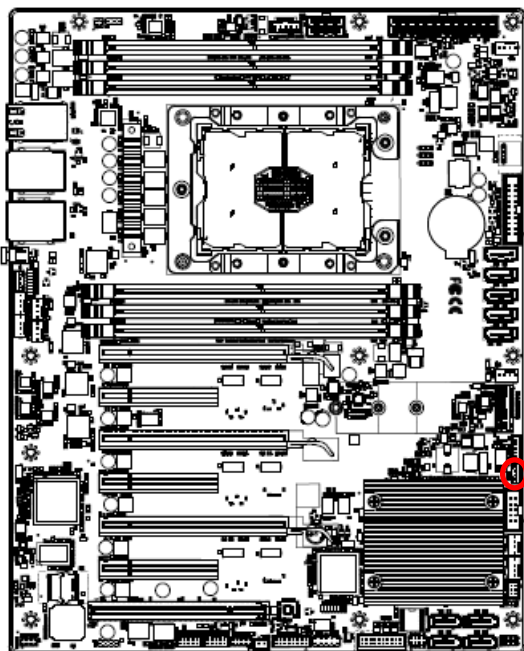
HPS-621UTA

2.4.27 Front Panel connector (JFP1)



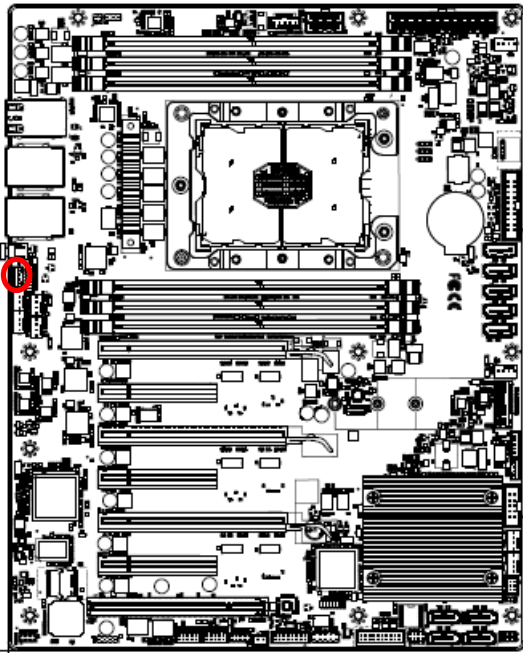
| Signal | PIN | PIN | Signal |
|----------------------|-----|-----|-----------------|
| LAN_LED_ACT# | 20 | 19 | GND |
| +3.3VSB | 18 | 17 | FP_UID_BTN_N_R |
| GND | 16 | 15 | FRONT_UID_LED_P |
| SBPWRLED_P | 14 | 13 | FRONT_UID_LED_N |
| LAN1_LED_ACT_n | 12 | 11 | STATUS_LED_N |
| LAN1_FRONT_LED_ACT_p | 10 | 9 | STATUS_LED_P |
| GND | 8 | 7 | GND |
| FP_PWR_BTN_N_R | 6 | 5 | FP_RST_BTN_N |
| PWRLED_N | 4 | 3 | HDD_LED_N |
| +3.3VSB | 2 | 1 | HDD_LED_P |

2.4.28 Inlet Thermal Sensors connector (INLET_SER1)



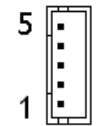
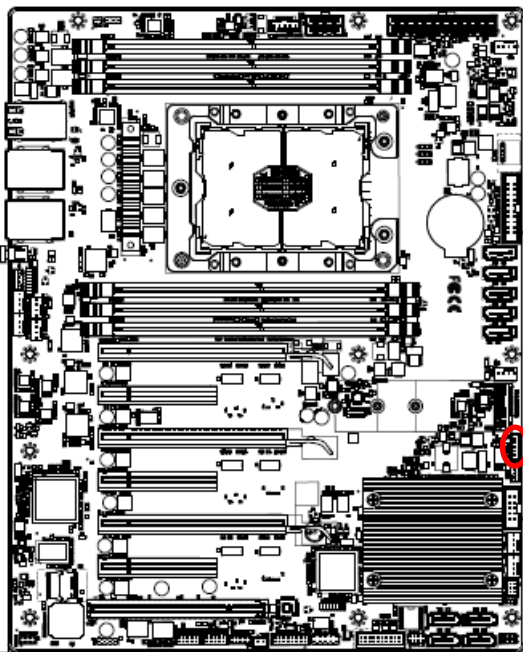
| Signal | PIN |
|-------------------------------|-----|
| GND | 4 |
| SMB1_TEMPSENSOR_STBY_LVC3_SCL | 3 |
| SMB1_TEMPSENSOR_STBY_LVC3_SDA | 2 |
| +3.3VSB | 1 |

2.4.29 Outlet Thermal Sensors connector (OUTLET_SER1)



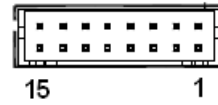
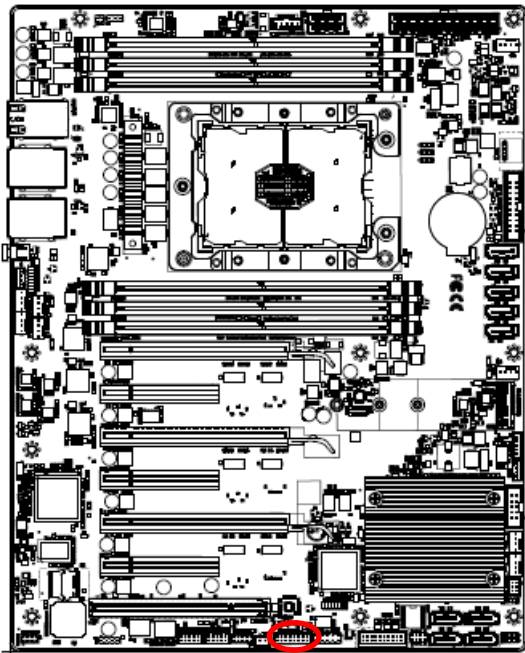
| Signal | PIN |
|------------------------------|-----|
| +3.3VSB | 1 |
| SMB_TEMPSENSOR_STBY_LVC3_SDA | 2 |
| SMB_TEMPSENSOR_STBY_LVC3_SCL | 3 |
| GND | 4 |

2.4.30 HDD Backplane thermal Sensors connector (HDD_SER1)



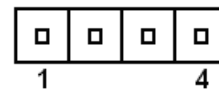
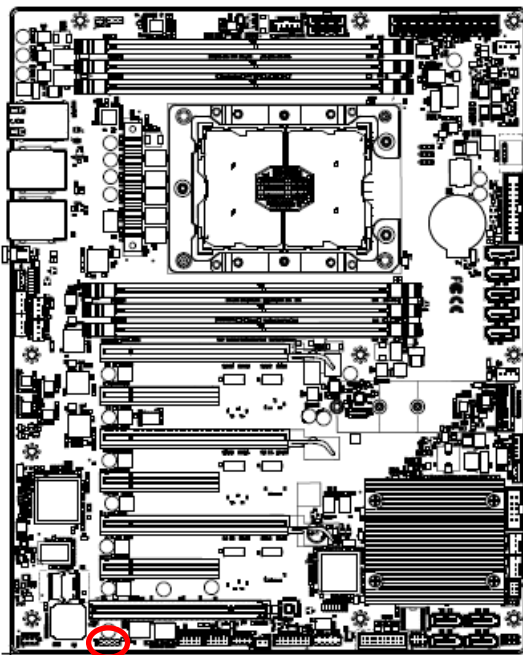
| Signal | PIN |
|-------------------------------|-----|
| SSD_LED_N | 5 |
| GND | 4 |
| SMB2_TEMPSENSOR_STBY_LVC3_SCL | 3 |
| SMB2_TEMPSENSOR_STBY_LVC3_SDA | 2 |
| +3.3VSB | 1 |

2.4.31 VGA connector (JVGA1)



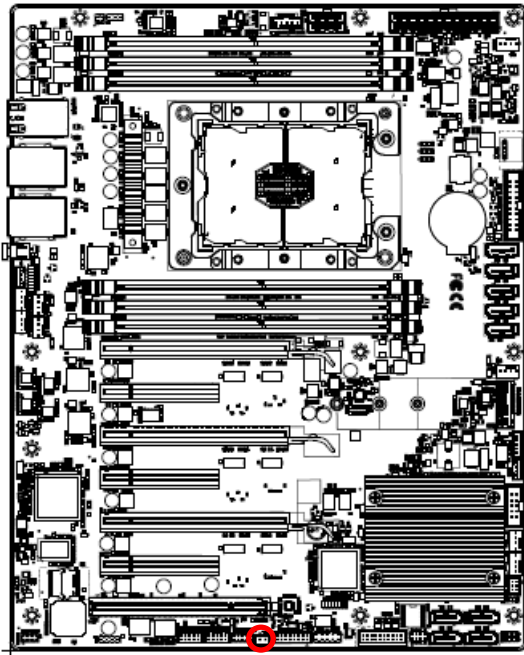
| Signal | PIN | PIN | Signal |
|-------------|-----|-----|----------------|
| CRT_z_RED | 2 | 1 | +5V |
| CRT_z_GREEN | 4 | 3 | GND |
| CRT_z_BLUE | 6 | 5 | NC |
| NC | 8 | 7 | CRT_DDC_z_DATA |
| GND | 10 | 9 | CRT_z_HSYNC |
| GND | 12 | 11 | CRT_z_VSYNC |
| GND | 14 | 13 | CRT_DDC_z_CLK |
| GND | 16 | 15 | GND |

2.4.32 For BMC debug message read (JBMC_UART1)



| Signal | PIN |
|----------|-----|
| UART5_TX | 1 |
| UART5_RX | 2 |
| GND | 3 |
| +3.3VSB | 4 |

2.4.33 CASE OPEN connector (JCASE_OPEN1)



| Signal | PIN |
|----------------------|-----|
| FP_CHASSIS_INTRUSION | 1 |
| GND | 2 |

3. BIOS Setup



3.1 Introduction

The BIOS setup program allows users to modify the basic system configuration. In this following chapter will describe how to access the BIOS setup program and the configuration options that may be changed.

3.2 Starting Setup

AMI BIOS™ is immediately activated when you first power on the computer. The BIOS reads the system information contained in the NVRAM and begins the process of checking out the system and configuring it. When it finishes, the BIOS will seek an operating system on one of the disks and then launch and turn control over to the operating system.

While the BIOS is in control, the Setup program can be activated in one of two ways:

By pressing <ESC> or immediately after switching the system on, or

By pressing the <ESC> or key when the following message appears briefly at the left-top of the screen during the POST (Power On Self Test).

Press <ESC> or to enter SETUP

If the message disappears before you respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the "RESET" button on the system case. You may also restart by simultaneously pressing <Ctrl>, <Alt>, and <Delete> keys.

3.3 Using Setup

In general, you use the arrow keys to highlight items, press <Enter> to select, use the PageUp and PageDown keys to change entries, press <F1> for help and press <Esc> to quit. The following table provides more detail about how to navigate in the Setup program using the keyboard.

| Button | Description |
|---------|---|
| ↑ | Move to previous item |
| ↓ | Move to next item |
| ← | Move to the item in the left hand |
| → | Move to the item in the right hand |
| Esc key | Main Menu -- Quit and not save changes into NVRAM Status Page Setup Menu and Option Page Setup Menu -- Exit current page and return to Main Menu |
| + key | Increase the numeric value or make changes |
| - key | Decrease the numeric value or make changes |
| F1 key | General help, only for Status Page Setup Menu and Option Page Setup Menu |
| F2 key | Previous Values |
| F3 key | Optimized defaults |
| F4 key | Save & Exit Setup |

- **Navigating Through The Menu Bar**

Use the left and right arrow keys to choose the menu you want to be in.



Note: Some of the navigation keys differ from one screen to another.

- **To Display a Sub Menu**

Use the arrow keys to move the cursor to the sub menu you want. Then press <Enter>. A “➤” pointer marks all sub menus.

3.4 Getting Help

Press F1 to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window press <Esc> or the <Enter> key again.

3.5 In Case of Problems

If, after making and saving system changes with Setup, you discover that your computer no longer is able to boot, the AMI BIOS supports an override to the NVRAM settings which resets your system to its defaults.

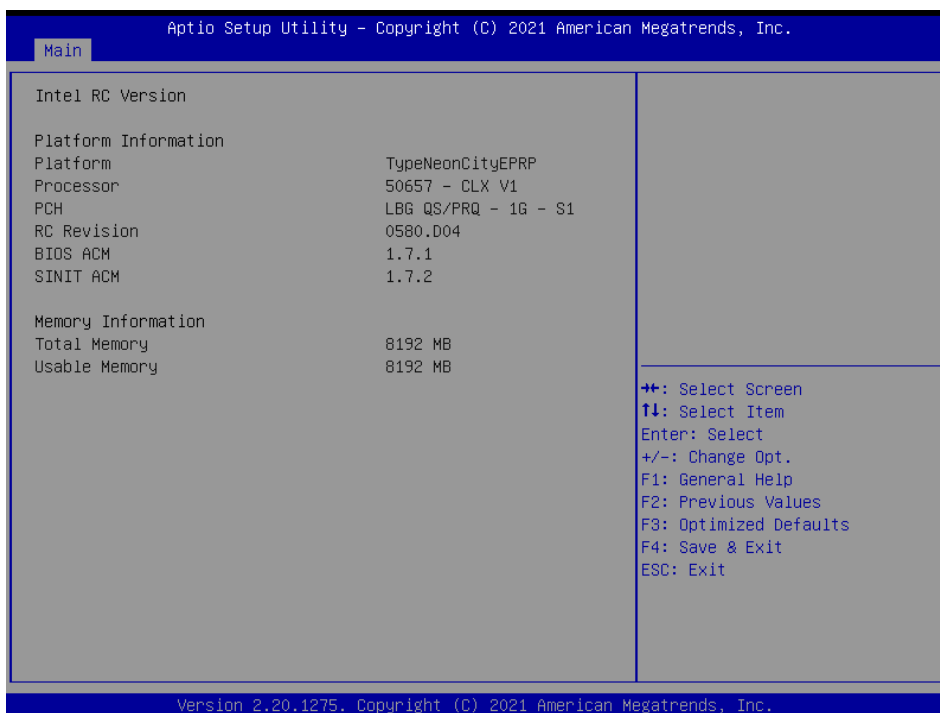
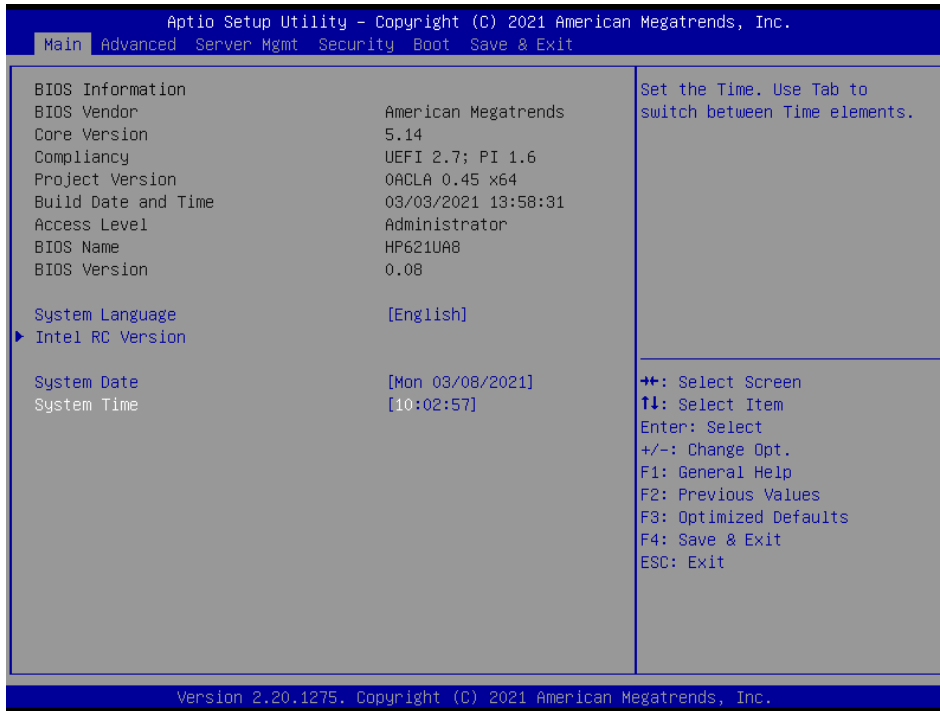
The best advice is to only alter settings which you thoroughly understand. To this end, we strongly recommend that you avoid making any changes to the chipset defaults. These defaults have been carefully chosen by both BIOS Vendor and your systems manufacturer to provide the absolute maximum performance and reliability. Even a seemingly small change to the chipset setup has the potential for causing you to use the override.

3.6 BIOS setup

Once you enter the Aptio Setup Utility, the Main Menu will appear on the screen. The Main Menu allows you to select from several setup functions and exit choices. Use the arrow keys to select among the items and press <Enter> to accept and enter the sub-menu.

3.6.1 Main Menu

This section allows you to record some basic hardware configurations in your computer and set the system clock.



3.6.1.1 System Language

This option allows choosing the system default language.

3.6.1.2 System Date

Use the system date option to set the system date. Manually enter the day, month and year.

3.6.1.3 System Time

Use the system time option to set the system time. Manually enter the hours, minutes and seconds.

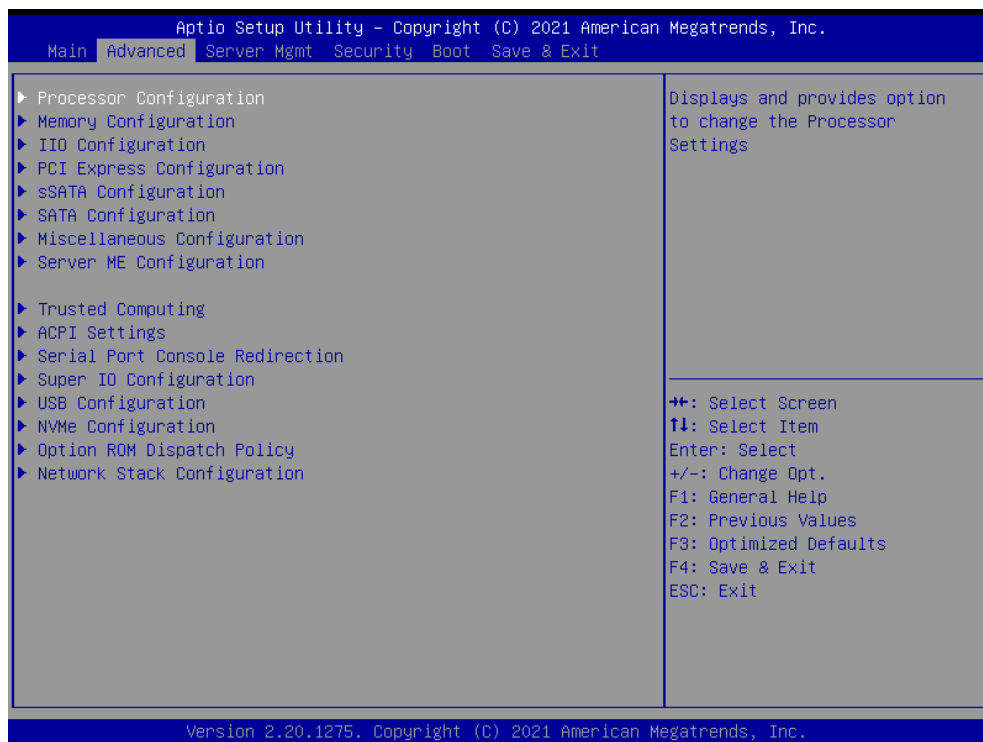


Note: The BIOS setup screens shown in this chapter are for reference purposes only, and may not exactly match what you see on your screen.

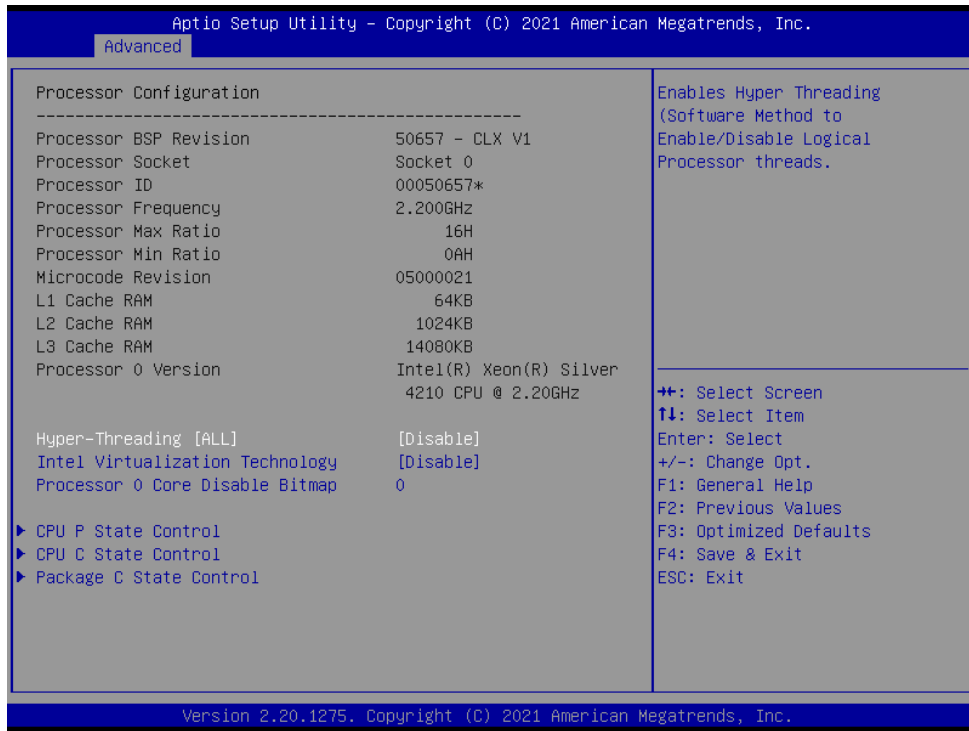
Visit the Avalue website (www.avalue.com.tw) to download the latest product and BIOS information.

3.6.2 Advanced Menu

This section allows you to configure your CPU and other system devices for basic operation through the following sub-menus.

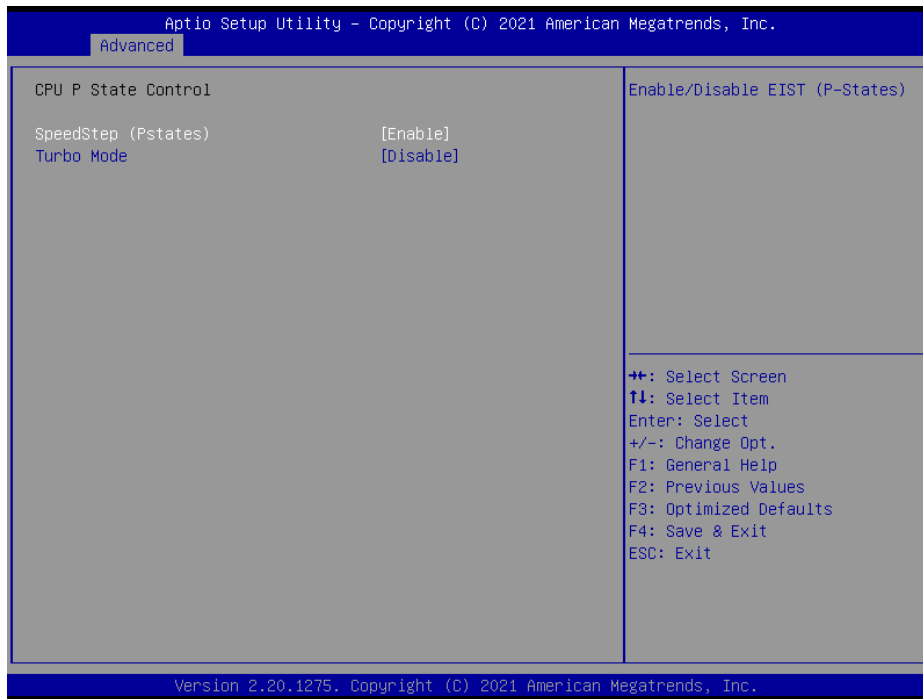


3.6.2.1 Processor Configuration



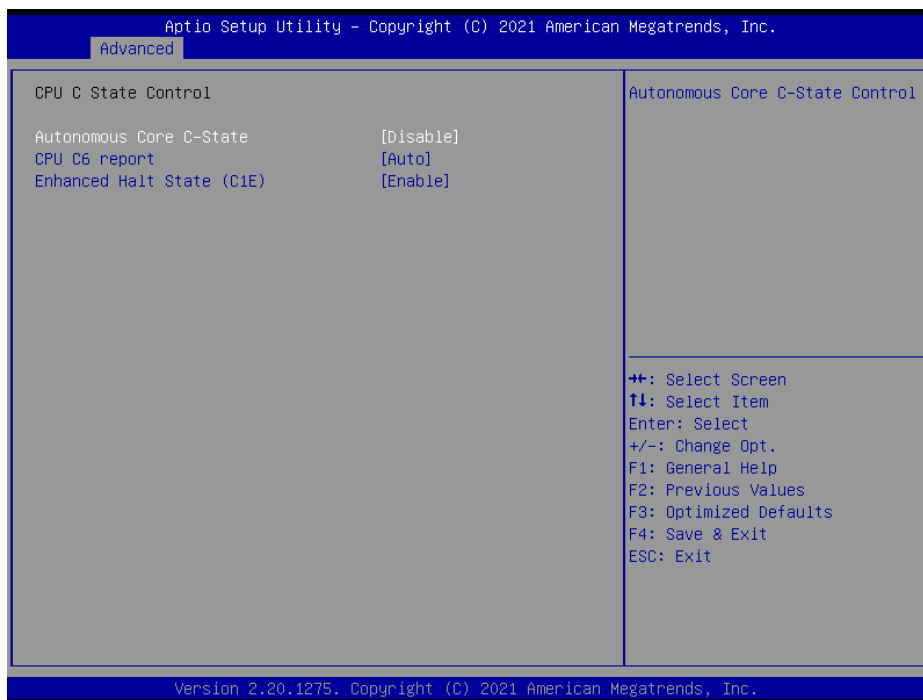
| Item | Options | Description |
|--|----------------------------|---|
| Hyper-Threading (ALL) | Disable[Default] Enable | Enables Hyper Threading (Software Method to Enable/Disable Logical Processor threads. |
| Intel Virtualization Technology | Disable[Default] Enable | Enables the Vanderpool Technology, takes effect after reboot. |
| Processor 0 Core Disable Bitmap | 0 | 0: Enable all cores. 3fff: Disable all cores. |

3.6.2.1.1 CPU P State Control



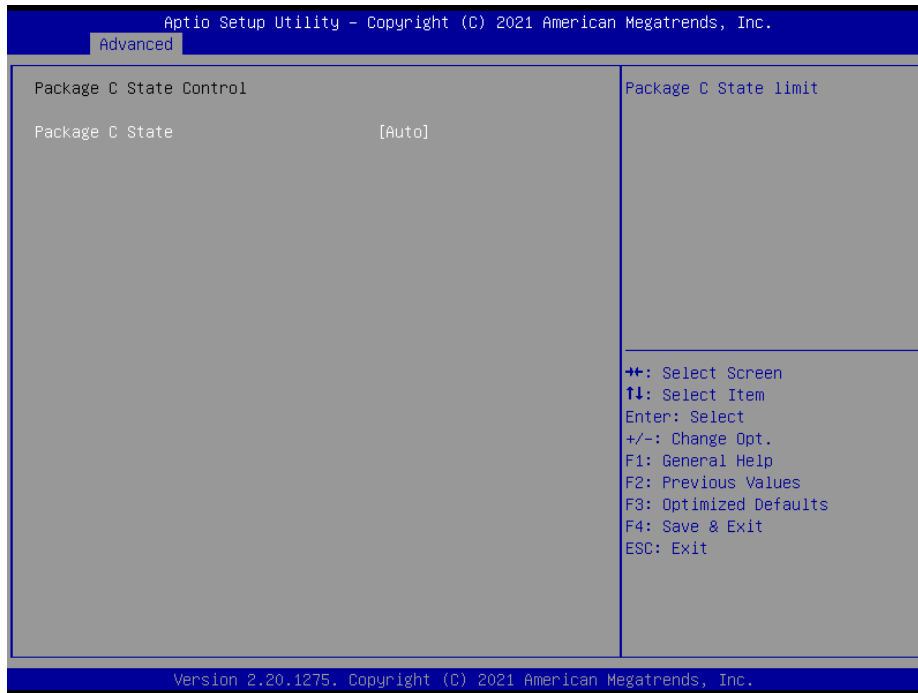
| Item | Option | Description |
|----------------------------|--------------------------------------|---|
| SpeedStep (Pstates) | Enable[Default], Disable | Enable/Disable EIST (P-States) |
| Turbo Mode | Enable Disable[Default] | Enable/Disable processor Turbo Mode (requires EMTTM enabled too). |

3.6.2.1.2 CPU C State Control



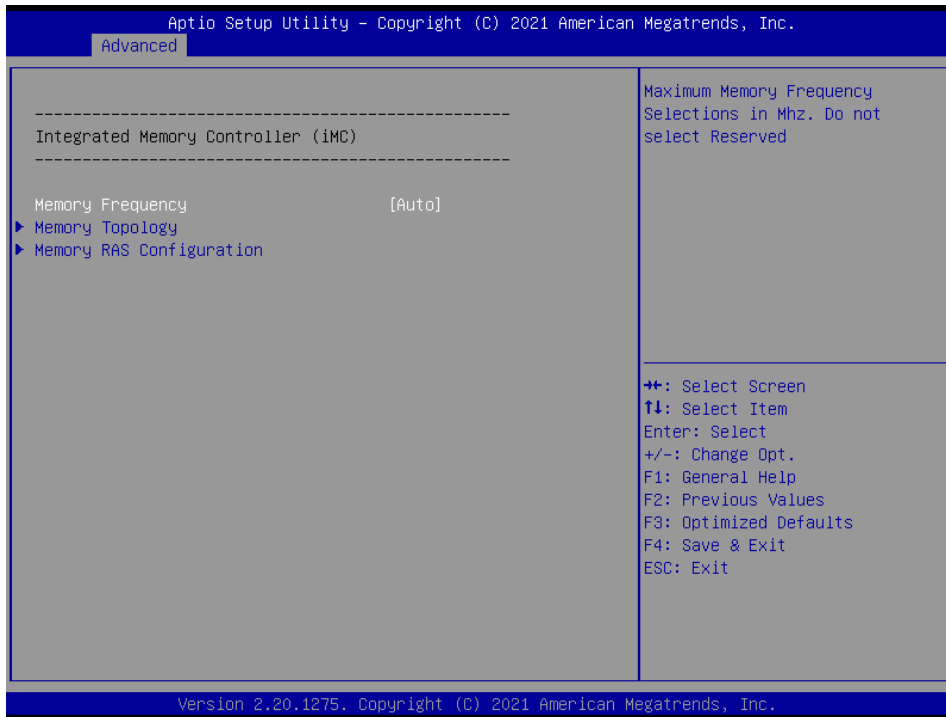
| Item | Option | Description |
|----------------------------------|--|---|
| Autonomous Core C-State | Enable Disable [Default] , | Autonomous Core C-State Control. |
| CPU C6 report | Disable Enable Auto [Default] | Enable/Disable CPU C6(ACPI C3) report to OS. |
| Enhanced Halt State (C1E) | Disable Enable [Default] | Core C1E auto promotion Control. Takes effect after reboot. |

3.6.2.1.3 Package C State Control



| Item | Option | Description |
|------------------------|--|------------------------|
| Package C State | C0/C1 state C2 state C6(non Retention)state C6(Retention)state No Limit Auto [Default] , | Package C State limit. |

3.6.2.2 Memory Configuration



| Item | Option | Description |
|-------------------------|--|---|
| Memory Frequency | Auto[Default]/800/1000/1066/1200 /1333/1400/1600/1800/1866 /2000/2133/2200/2400/2600/2666 /2800-OvrClk/2933/3000-OvrClk /3200-OvrClk/3400-OvrClk/3466-OvrClk /3600-OvrClk/3733-OvrClk/3800-OvrClk /4000-OvrClk/4200-OvrClk/4266-OvrClk /4400-OvrClk | Maximum Memory Frequency Selections in Mhz. Do not select Reserved. |

3.6.2.2.1 Memory Topology



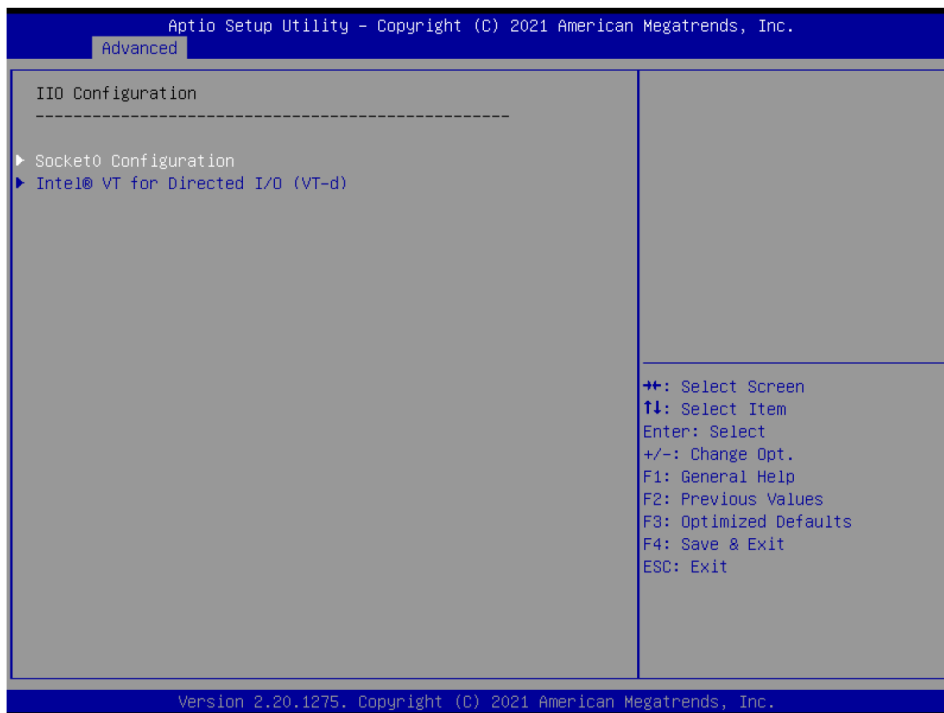
3.6.2.2.2 Memory RAS Configuration



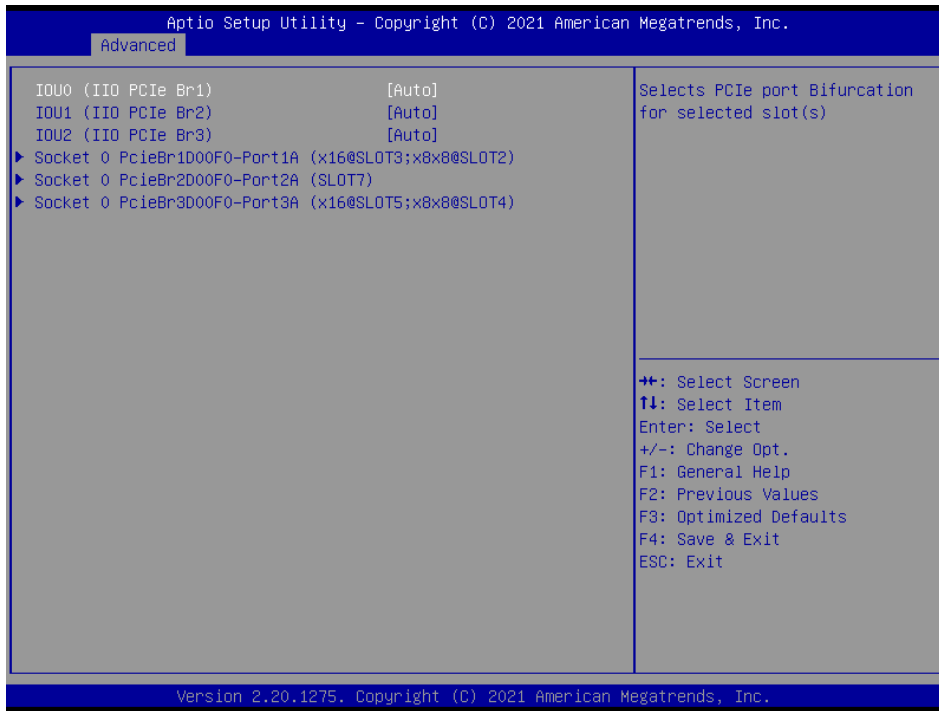
| Item | Option | Description |
|-------------------------------------|---|---|
| Static Virtual Lockstep Mode | Disable[Default] Enable | Enable Static Virtual Lockstep mode. |
| Mirror mode | Disable[Default] Enable Mirror Mode(1LM) | Mirror Mode will set entire 1LM memory in system to be mirrored, consequently reducing the memory |

| | | |
|------------------------------------|---|---|
| | | capacity by half. Mirror Enable will disable XPT Prefetch. |
| UEFI ARM Mirror | Disable[Default] Enable | Imitate behaviour of UEFI based Address Range Mirror with setup option. |
| Memory Rank Sparing | Disable[Default] Enable | Enable/Disable Memory Rank Sparing. This feature is only available on 1LM. |
| Correctable Error Threshold | 32767 | Correctable Error Threshold (0x01 – 0x7fff) used for sparing, tagging, and leaky bucket. |
| SDDC | Disable[Default] Enable | Enable/Disable SDDC. Not supported when AEP dim present! |
| ADDDC Sparing | Disable[Default] Enable | Enable/Disable ADDDC Sparing. |
| Set NGN Die Sparing | Disable Enable[Default] | Enable/Disable NGN Die Sparing. |
| Patrol Scrub | Disable Enable[Default] | Enable/Disable Patrol Scrub. |
| Patrol Scrub Interval | 1-24[Default] | Selects the number of hours (1-24) required to complete full scrub. A value of zero means auto! |
| Patrol Scrub Address Mode | Reverse Address System Physical Address[Default] | Selects the address mode between System Physical Address (or) Reverse Address. |

3.6.2.3 IIO Configuration



3.6.2.3.1 Socket0 Configuration



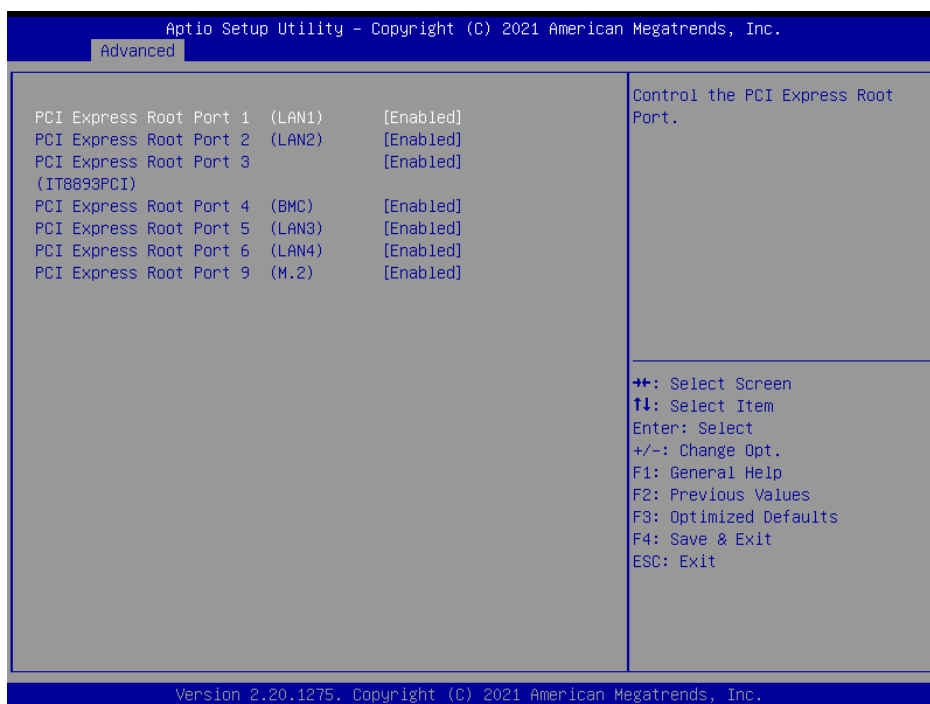
| Item | Options | Description |
|---------------------|------------------------------|--|
| IOU0 (IIO PCIe Br1) | x8x8 x16 Auto[Default] | Select PCIe port Bifurcation for selected slot(s). |
| IOU1 (IIO PCIe Br2) | x8x8 x16 Auto[Default] | Select PCIe port Bifurcation for selected slot(s). |
| IOU2 (IIO PCIe Br3) | x8x8 x16 Auto[Default] | Select PCIe port Bifurcation for selected slot(s). |

3.6.2.3.2 Intel® VT for Directed I/O (VT-d)



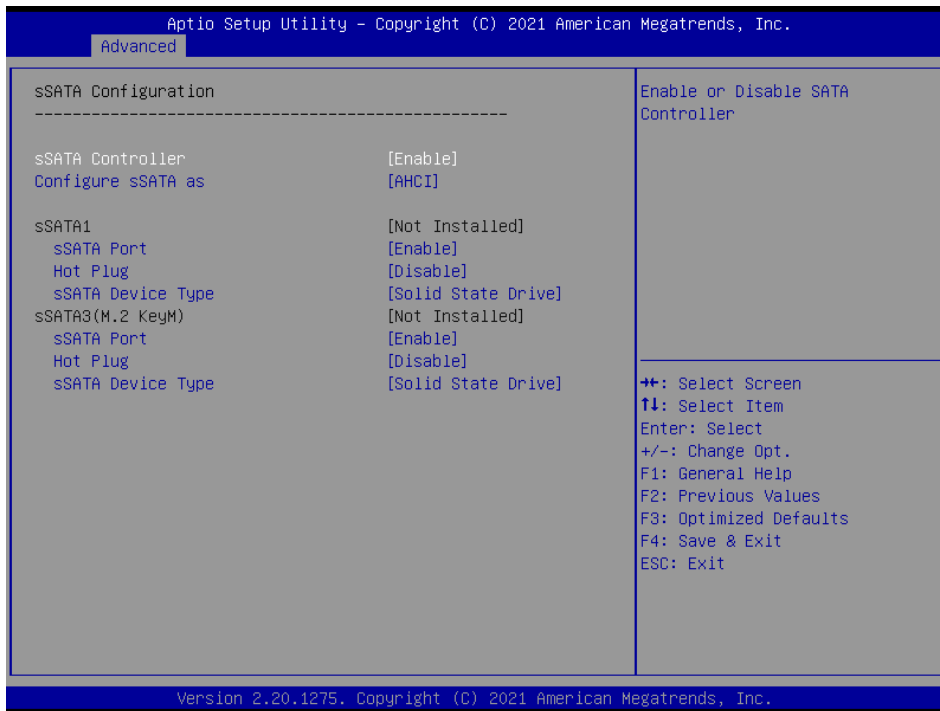
| Item | Options | Description |
|-----------------------------------|----------------------------|---|
| Intel® VT for Directed I/O (VT-d) | Enable[Default] Disable | Enable/Disable Intel® Virtualization Technology for Directed I/O (VT-d) by reporting the I/O device assignment to VMM through DMAR ACPI Tables. |

3.6.2.4 PCI Express Configuration



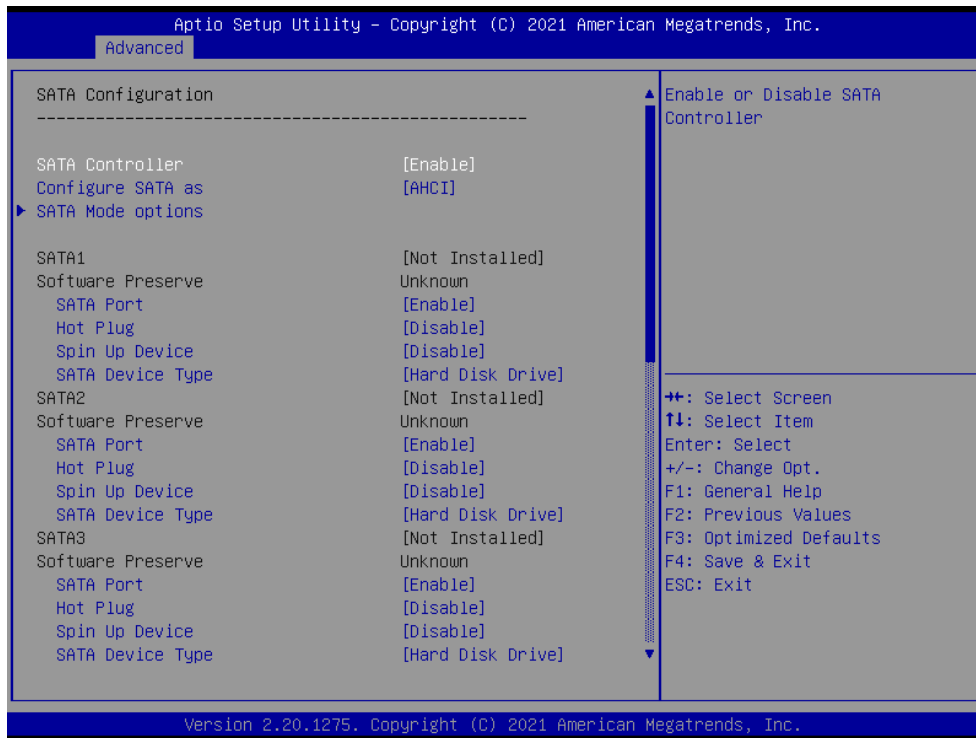
| Item | Options | Description |
|------------------------------------|------------------------------|------------------------------------|
| PCI Express Root Port 1(LAN1) | Disabled Enabled[Default] | Control the PCI Express Root Port. |
| PCI Express Root Port 2(LAN2) | Disabled Enabled[Default] | Control the PCI Express Root Port. |
| PCI Express Root Port 3(IT8893PCI) | Disabled Enabled[Default] | Control the PCI Express Root Port. |
| PCI Express Root Port 4(BMC) | Disabled Enabled[Default] | Control the PCI Express Root Port. |
| PCI Express Root Port 5(LAN3) | Disabled Enabled[Default] | Control the PCI Express Root Port. |
| PCI Express Root Port 6(LAN4) | Disabled Enabled[Default] | Control the PCI Express Root Port. |
| PCI Express Root Port 9(M.2) | Disabled Enabled[Default] | Control the PCI Express Root Port. |

3.6.2.5 sSATA Configuration



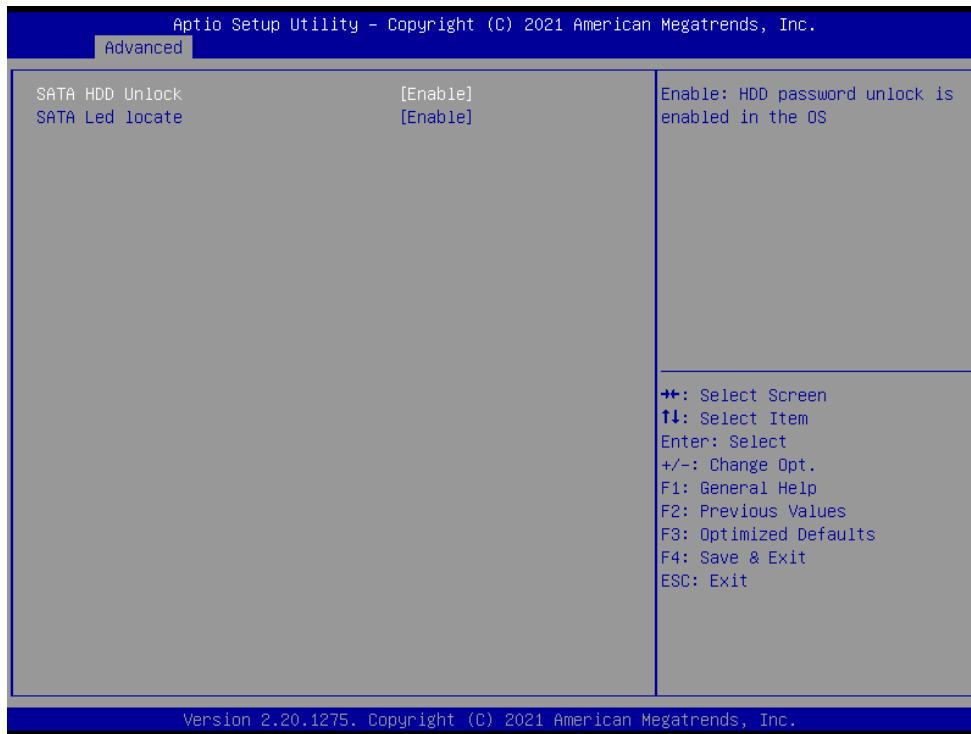
| Item | Options | Description |
|--------------------|---|--|
| sSATA Controller | Enable[Default] Disable | Enable or Disable SATA Controller. |
| Configure sSATA as | AHCI[Default] RAID | This will configure sSATA as RAID or AHCI. |
| sSATA Port | Disable Enable[Default] | Enable or Disable SATA Port. |
| Hot Plug | Disable[Default] Enable | Designates this port as Hot Pluggable. |
| sSATA Device Type | Hard Disk Drive Solid State Drive[Default] | Identify the SATA port is connected to Solid State Drive or Hard Disk Drive. |

3.6.2.6 SATA Configuration



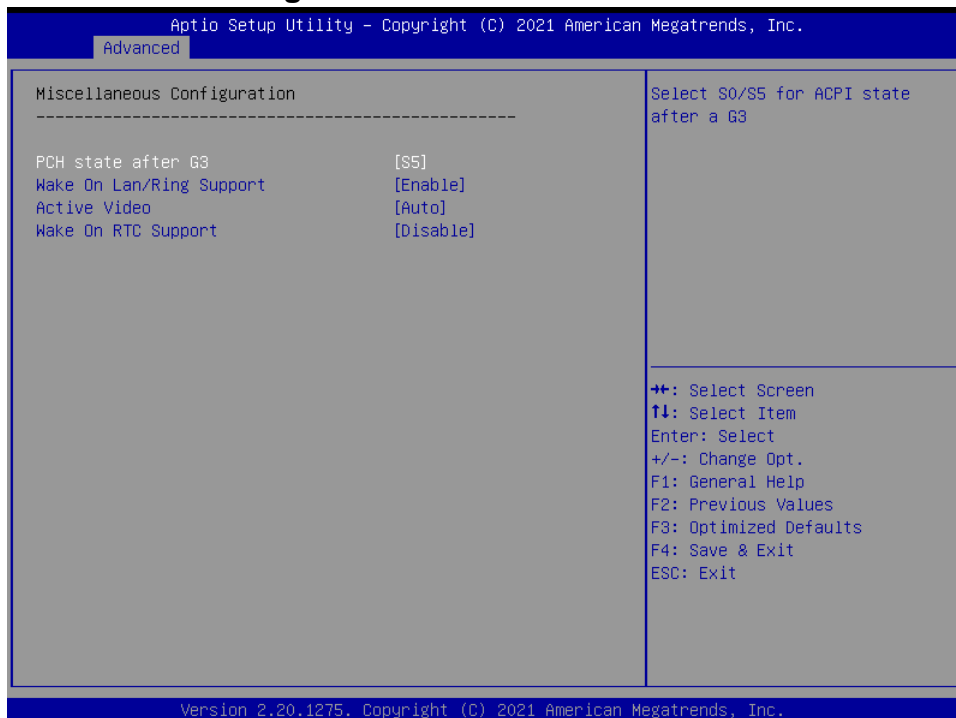
| Item | Options | Description |
|--------------------------|---|--|
| SATA Controller | Enable[Default] Disable | Enable or Disable SATA Controller. |
| Configure SATA as | AHCI[Default] RAID | This will configure SATA as RAID or AHCI |
| SATA Port | Disable Enable[Default] | Enable or Disable SATA Port. |
| Hot Plug | Disable[Default] Enable | Designates this port as Hot Pluggable. |
| Spin Up Device | Disable[Default] Enable | If enabled for any of ports Staggered Spin Up will be performed and only the drives witch have this option enabled will spin up at boot. Otherwise all drives spin up at boot. |
| SATA Device Type | Hard Disk Drive[Default] Solid State Drive | Identify the SATA port is connected to Solid State Drive or Hard Disk Drive. |

3.6.2.6.1 SATA Mode options



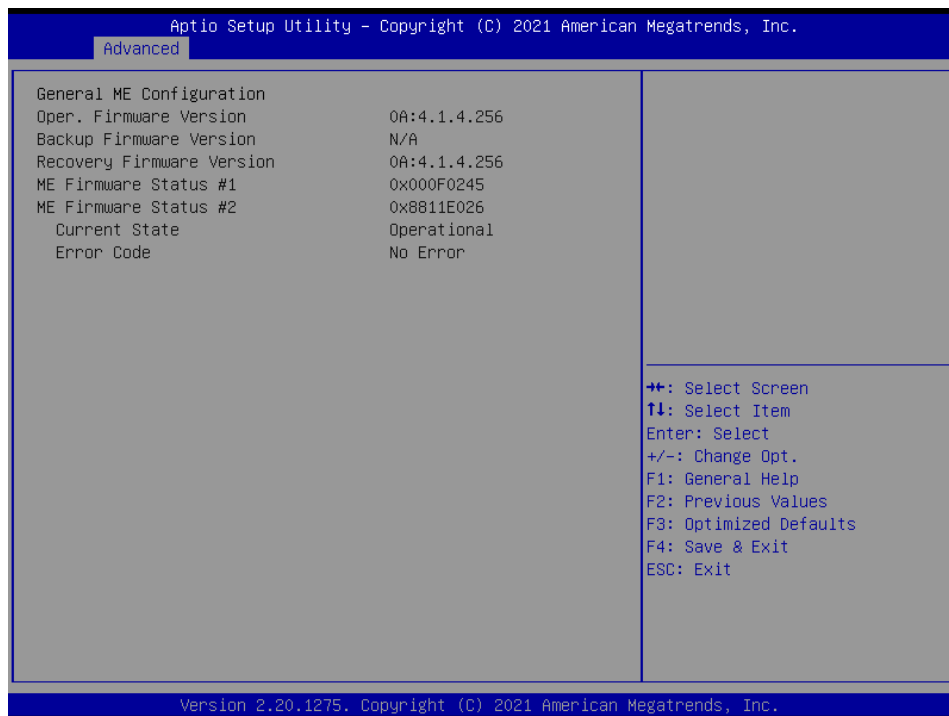
| Item | Option | Description |
|-----------------|----------------------------|---|
| SATA HDD Unlock | Disable Enable[Default] | Enable: HDD password unlock is enabled in the OS. |
| SATA Led locate | Disable Enable[Default] | If enabled LED/SGPIO hardware is attached. |

3.6.2.7 Miscellaneous Configuration



| Item | Options | Description |
|---------------------------------|--|---|
| PCH state after G3 | S0 S5[Default] Leave power state unchanged | Select S0/S5 for ACPI state after a G3. |
| Wake On Lan/Ring Support | Disable, Enable[Default] | Enable or Disable Wake On Lan Support. |
| Active Video | Auto[Default] Onboard Offboard | Select active Video type. |
| Wake On RTC Support | Disable[Default], Enable | Enable or disable System wake on alarm event. When enabled, System will wake on the day ::hr::min::sec specified. |

3.6.2.8 Server ME Configuration

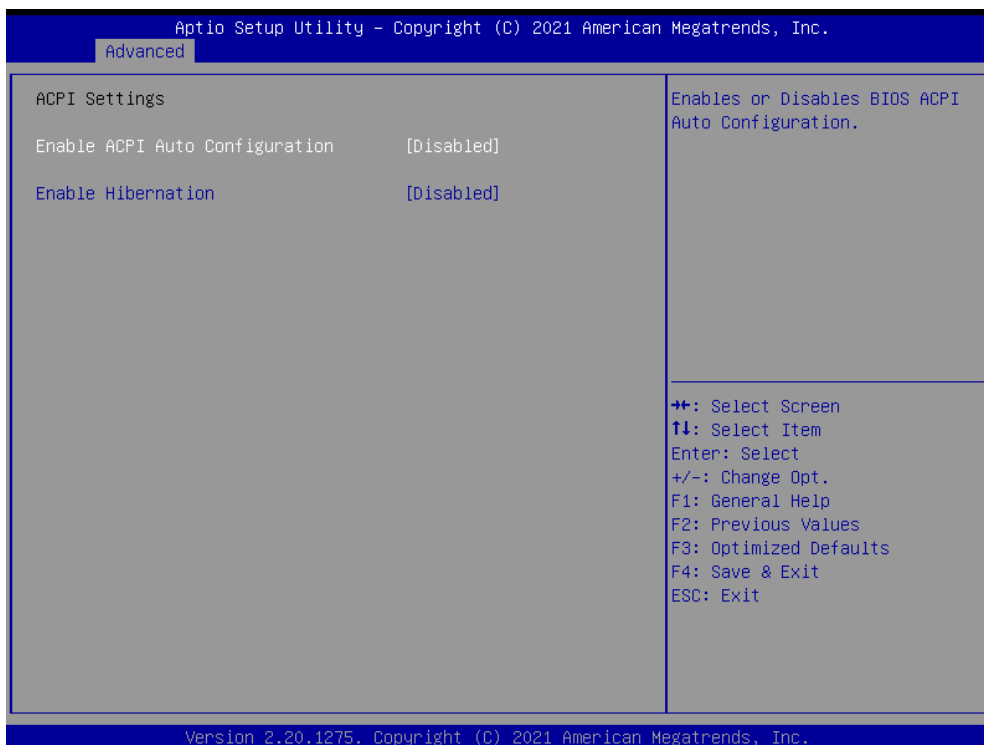


3.6.2.9 Trusted Computing



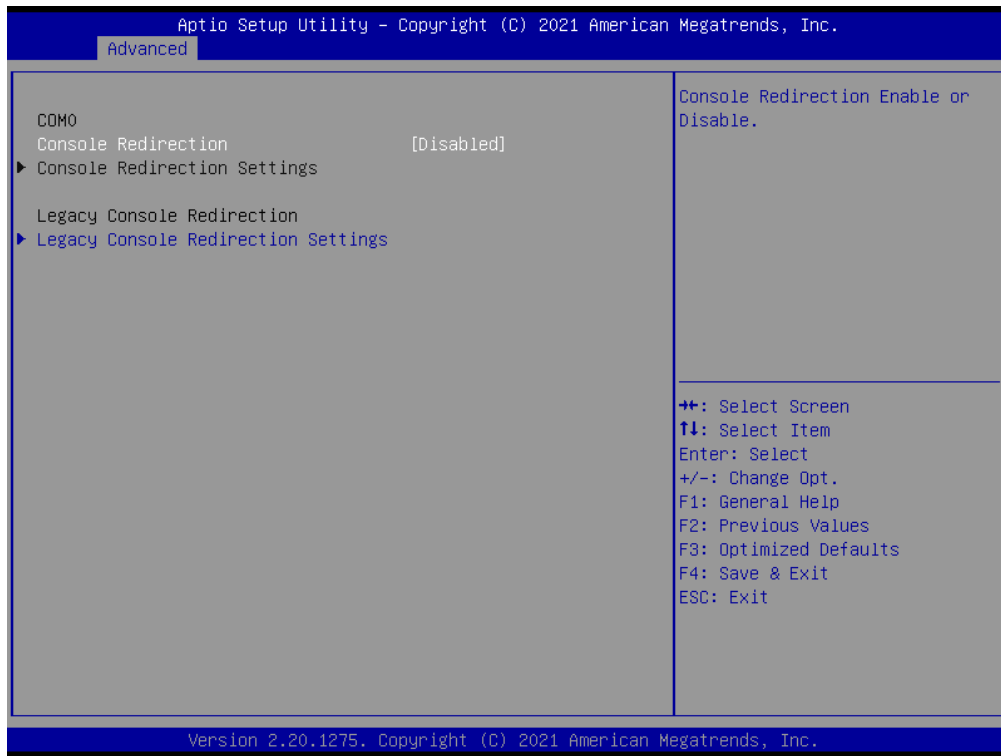
| Item | Options | Description |
|-------------|--------------------------|---|
| TPM Support | Disable, Enable[Default] | Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available. |

3.6.2.10 ACPI Settings



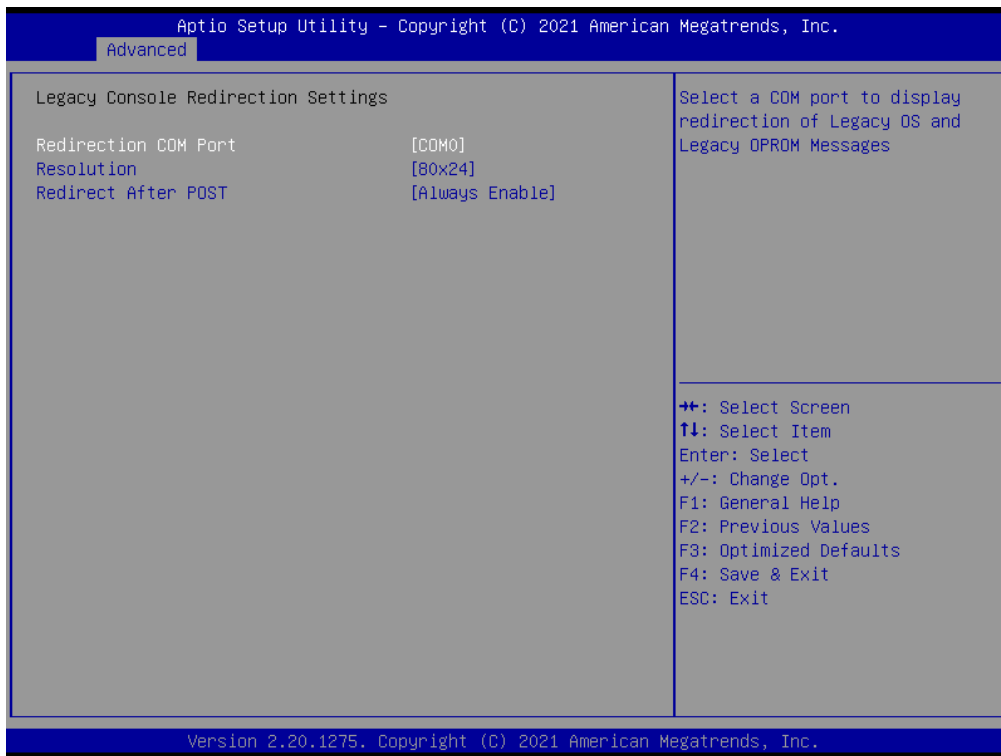
| Item | Options | Description |
|---------------------------------------|------------------------------|--|
| Enable ACPI Auto Configuration | Disabled[Default] Enabled | Enables or Disables BIOS ACPI Auto Configuration. |
| Enable Hibernation | Disabled[Default] Enabled | Enables or Disables System ability to Hibernate (OS/S4 Sleep State). This option may not be effective with some operating systems. |

3.6.2.11 Serial Port Console Redirection



| Item | Options | Description |
|----------------------------|-------------------------------|--|
| Console Redirection | Disabled[Default], Enabled | Console Redirection Enable or Disable. |

3.6.2.11.1 Legacy Console Redirection Settings



| Item | Option | Description |
|-----------------------------|--------------------------------------|--|
| Redirection COM Port | COM0[Default] | Select a COM port to display redirection of Legacy OS and Legacy OPRM Messages. |
| Resolution | 80x24[Default] 80x25 | On Legacy OS, the Number of Rows and Columns supported redirection. |
| Redirect After POST | Always Enable[Default] BootLoader | When Bootloader is selected, then Legacy Console Redirection is disabled before booting to legacy OS. When Always Enable is selected, then Legacy Console Redirection is enabled for legacy OS. Default setting for this option is set to Always Enable. |

3.6.2.12 Super IO Configuration



| Item | Description |
|------------------------------------|---|
| Serial Port 1 Configuration | Set Parameters of Serial Port 1 (COMA). |
| Serial Port 2 Configuration | Set Parameters of Serial Port 2 (COMB). |

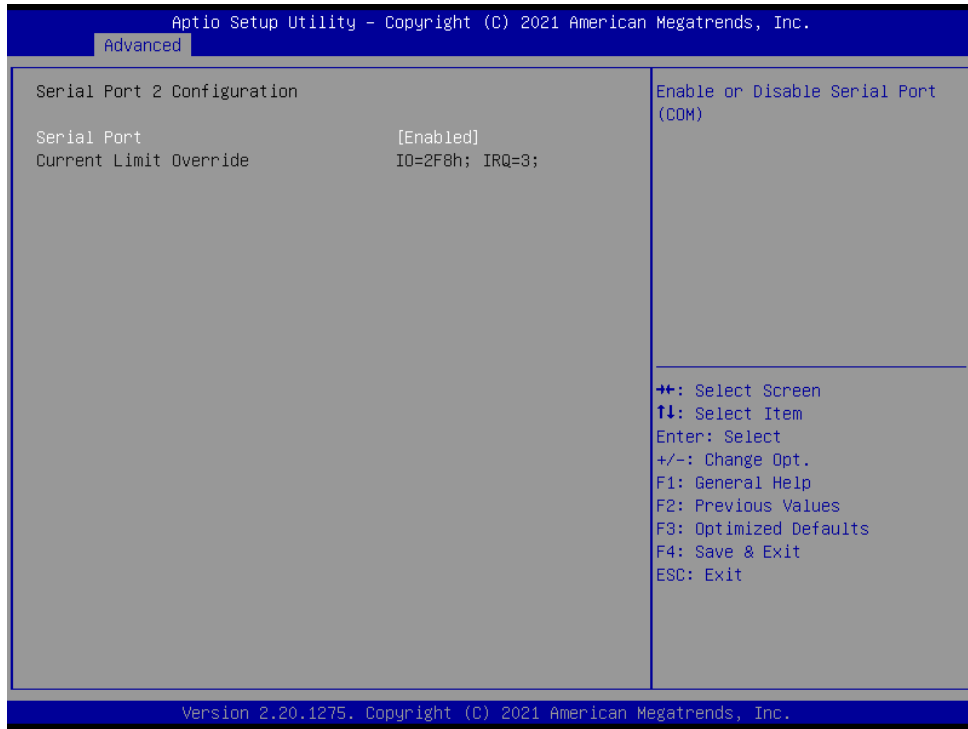
3.6.2.12.1 Serial Port 1 Configuration



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| Item | Option | Description |
|-------------|-------------------------------|--------------------------------------|
| Serial Port | Enabled[Default], Disabled | Enable or Disable Serial Port (COM). |

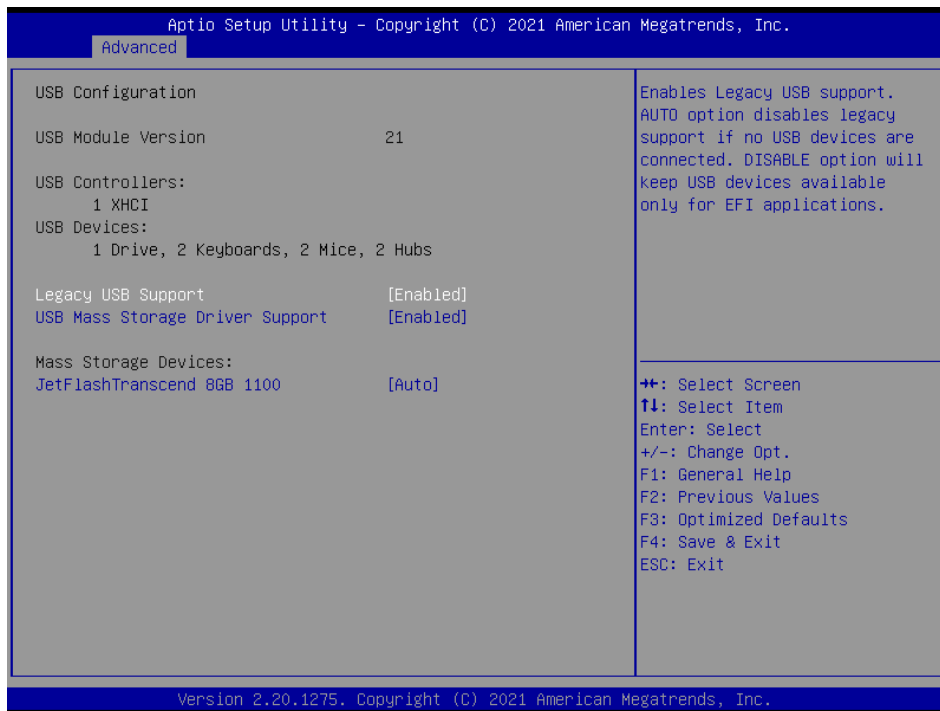
3.6.2.12.2 Serial Port 2 Configuration



| Item | Option | Description |
|-------------|-------------------------------|--------------------------------------|
| Serial Port | Enabled[Default], Disabled | Enable or Disable Serial Port (COM). |

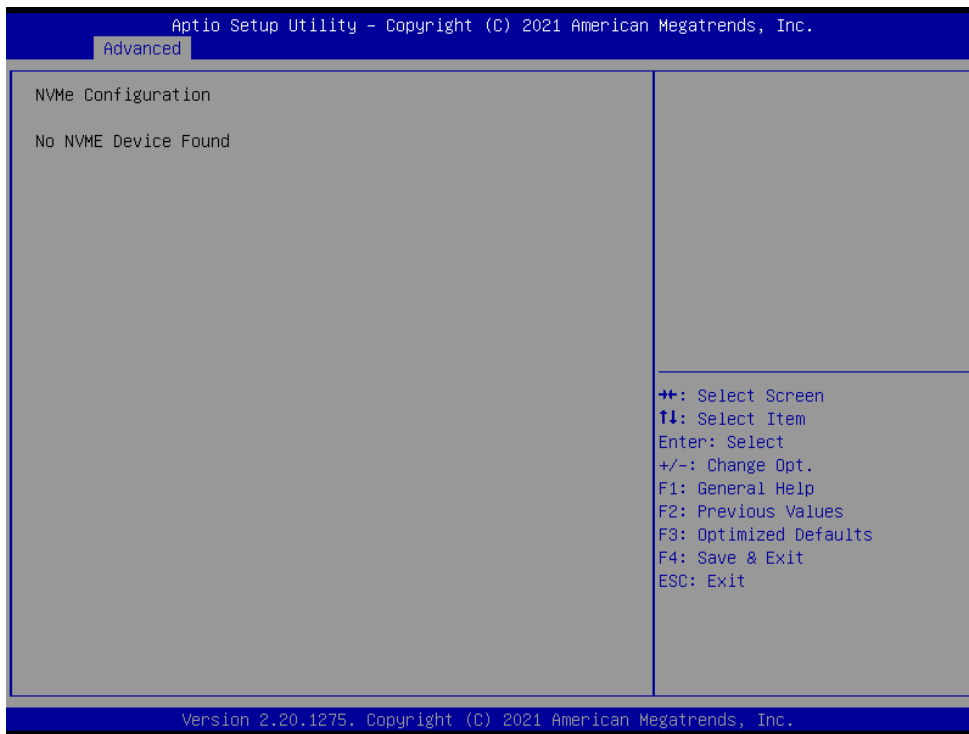
3.6.2.13 USB Configuration

The USB Configuration menu helps read USB information and configures USB settings.

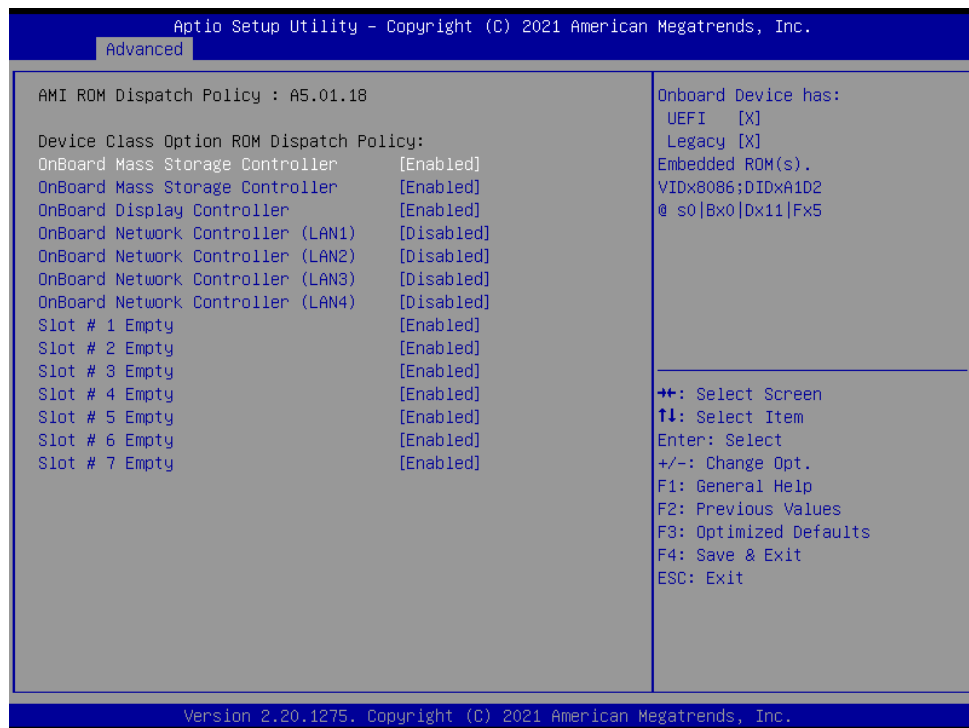


| Item | Options | Description |
|--|--|---|
| Legacy USB Support | Enabled[Default], Disabled Auto | Enables Legacy USB support. AUTO option disables legacy support if no USB devices are connected. DISABLE option will keep USB devices available only for EFI applications. |
| USB Mass Storage Driver Support | Disabled Enabled[Default], | Enable/Disable USB Mass Storage Driver Support. |
| Mass Storage Devices | Auto[Default] Floppy Forced FDD Hard Disk CD-ROM | Mass storage device emulation type. 'AUTO' enumerates devices according to their media format. Optical drives are emulated as 'CDROM', drives with no media will be emulated according to a drive type. |

3.6.2.14 NVMe Configuration



3.6.2.15 Option ROM Dispatch Policy

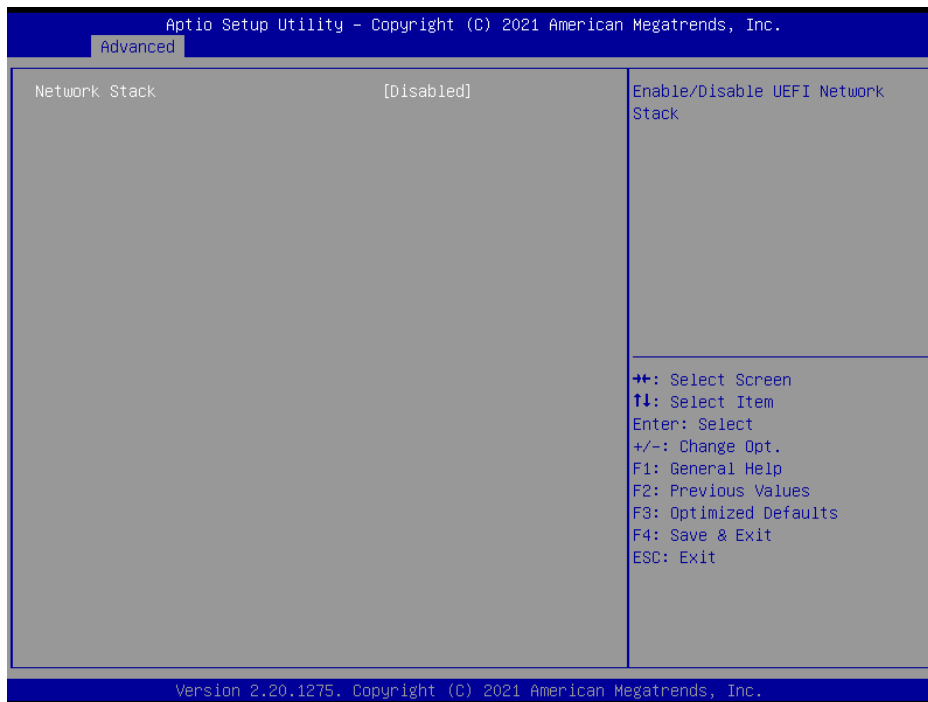


| Item | Options | Description |
|---------------------------------|----------------------------|---|
| Onboard Mass Storage Controller | Enabled[Default], Disabled | Onboard Device has: UEFI [X] Legacy [X] |

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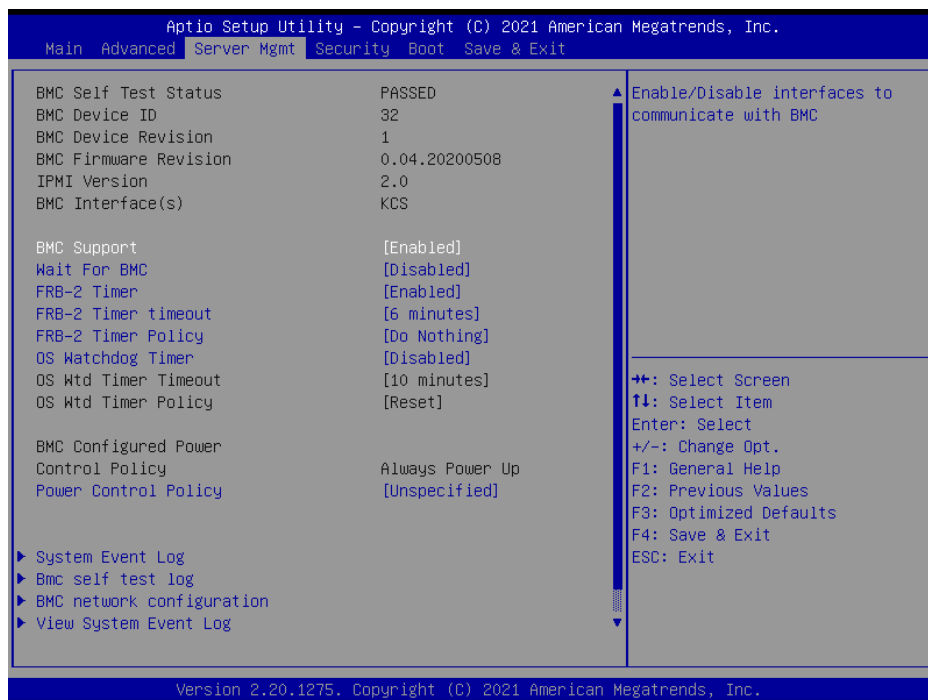
| | | |
|---|--|---|
| | | Embedded ROM(s). VIDx8086; DIDxA1D2 @ s0 Bx0 Dx11 Fx5 |
| Onboard Display Controller | Enabled[Default], Disabled | Onboard Device has: UEFI [X] Legacy [X] Embedded ROM(s). VIDx1A03; DIDx2000 @ s0 BxA Dx0 Fx0 |
| Onboard Network Controller(LAN1) | Enabled[Default], Disabled | Onboard Device has: UEFI [X] Legacy [X] Embedded ROM(s). VIDx8086; DIDx1533 @ s0 Bx6 Dx0 Fx0 |
| Onboard Network Controller(LAN2) | Enabled, Disabled[Default] | Onboard Device has: UEFI [X] Legacy [X] Embedded ROM(s). VIDx8086; DIDx1533 @ s0 Bx7 Dx0 Fx0 |
| Onboard Network Controller(LAN3) | Enabled, Disabled[Default] | Onboard Device has: UEFI [X] Legacy [X] Embedded ROM(s). VIDx8086; DIDx1533 @ s0 Bx1 Dx0 Fx0 |
| Onboard Network Controller(LAN4) | Enabled, Disabled[Default] | Onboard Device has: UEFI [X] Legacy [X] Embedded ROM(s). VIDx8086; DIDx1533 @ s0 Bx2 Dx0 Fx0 |
| Slot#1 Empty | Enabled[Default], Disabled | Enable or Disable Option ROM execution for selected Slot. |
| Slot#2 Bridge Device | Enabled[Default], Disabled | Enable or Disable Option ROM execution for selected Slot. |
| Slot#3 Empty | Enabled[Default], Disabled | Enable or Disable Option ROM execution for selected Slot. |
| Slot#4 Empty | Enabled[Default], Disabled | Enable or Disable Option ROM execution for selected Slot. |
| Slot#5 Empty | Enabled[Default], Disabled | Enable or Disable Option ROM execution for selected Slot. |
| Slot#6 Empty | Enabled[Default], Disabled | Enable or Disable Option ROM execution for selected Slot. |
| Slot#7 Empty | Enabled[Default], Disabled | Enable or Disable Option ROM execution for selected Slot. |

3.6.2.16 Network Stack Configuration



| Item | Options | Description |
|---------------|------------------------------|------------------------------------|
| Network Stack | Enabled Disabled[Default] | Enable/Disable UEFI Network Stack. |

3.6.3 Server Mgmt



| Item | Options | Description |
|-----------------------------|---|---|
| BMC Support | Enabled[Default] Disabled | Enable/Disable interfaces to communicate with BMC. |
| Wait For BMC | Enabled Disabled[Default] | Wait For BMC response for specified time out. BMC starts at the same time when BIOS starts during AC power ON. It takes around 30 seconds to initialize Host to BMC interfaces. |
| FRB-2 Timer | Enabled[Default] Disabled | Enable or Disable FRB-2 time (POST timer). |
| FRB-2 Timer timeout | 3 minutes 4 minutes 5 minutes 6 minutes[Default] | Enter value Between 3 to 6 min for FRB-2 Timer Expiration value. |
| FRB-2 Timer Policy | Do Nothing[Default] Reset Power Down Power Cycle | Configure how the system should respond if the FRB-2 Timer expires. Not available if FRB-2 Timer is disabled. |
| OS Watchdog Timer | Enabled Disabled[Default] | If enabled, starts a BIOS timer which can only be shut off by Management Software after the OS loads. Helps determine that the OS successfully loaded or follows the OS Boot Watchdog Timer policy. |
| Power Control Policy | Do Not PowerUp Last Power State Power Restore Unspecified[Default] | Configure how the system should respond if AC Power is lost, Reset not required as selected Power policy will be set in BMC when policy is saved. |

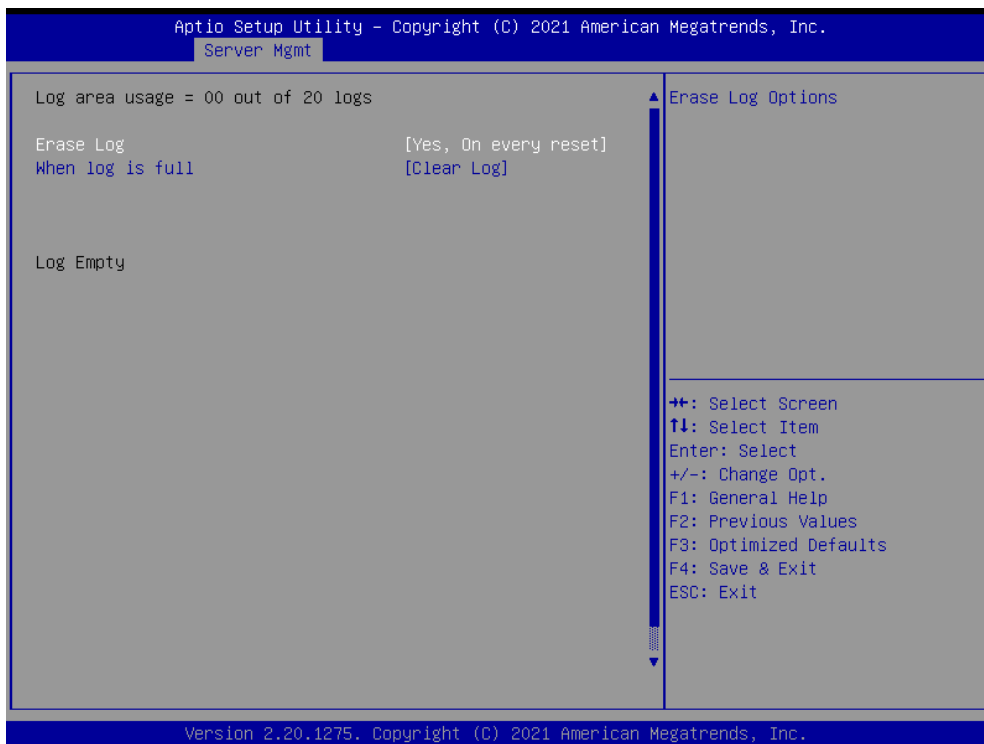
3.6.3.1 System Event Log



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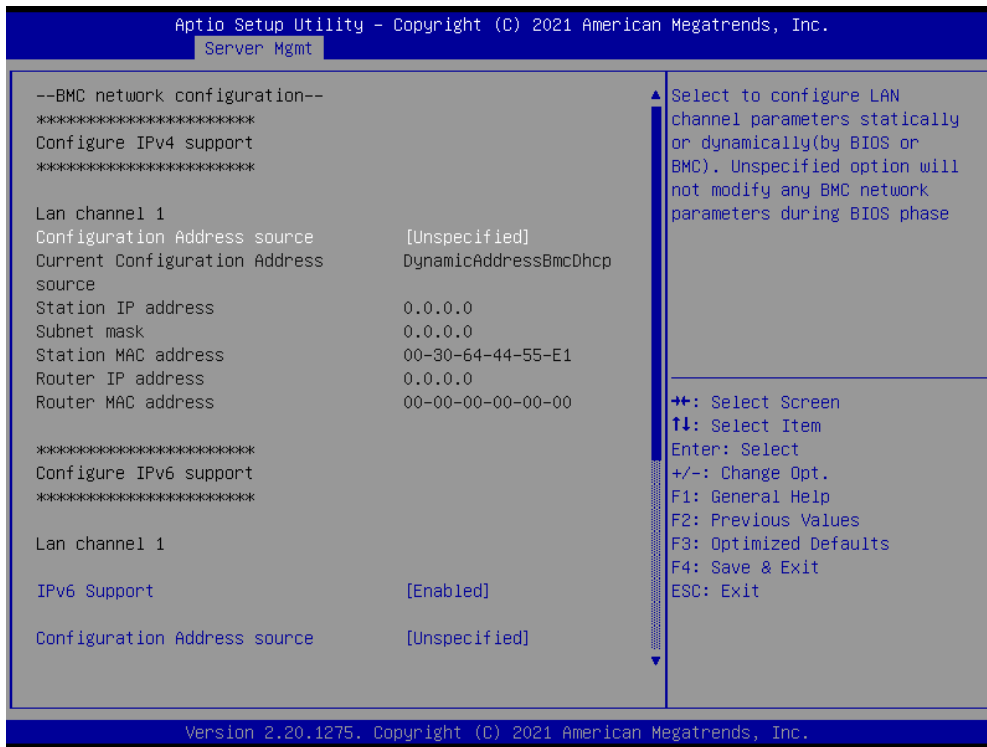
| Item | Option | Description |
|-----------------------------|--|---|
| SEL Components | Enabled[Default] Disabled | Change this to enable or disable event logging for error/progress codes during boot. |
| Erase SEL | No[Default] Yes, On next reset Yes, On every reset | Choose options for erasing SEL. |
| When SEL is Full | Do Nothing Erase Immediately Delete Oldest Record[Default] | Choose options for reactions to a full SEL. |
| Log EFI Status Codes | Disabled Both[Default] Error code Progress code | Disable the logging of EFI Status Codes or log only error code or only progress code or both. |

3.6.3.2 Bmc self test log



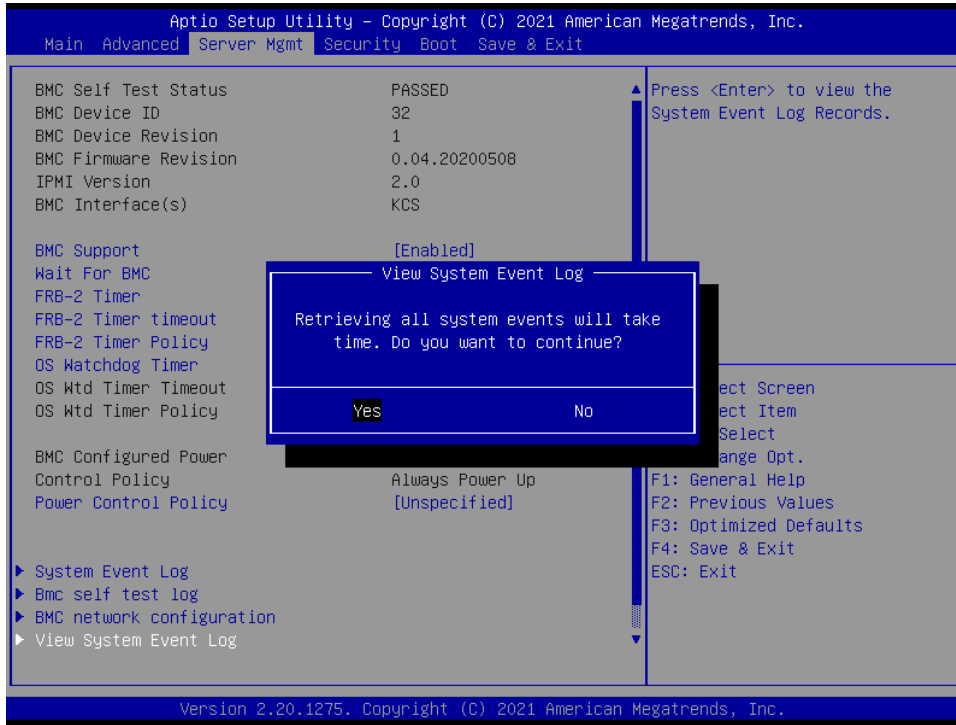
| Item | Option | Description |
|-------------------------|---|---|
| Erase Log | Yes, On every reset[Default] No | Erase Log Options. |
| When log is full | Clear Log[Default] Do not log any more | Select the action to be taken when log is full. |

3.6.3.3 BMC network configuration

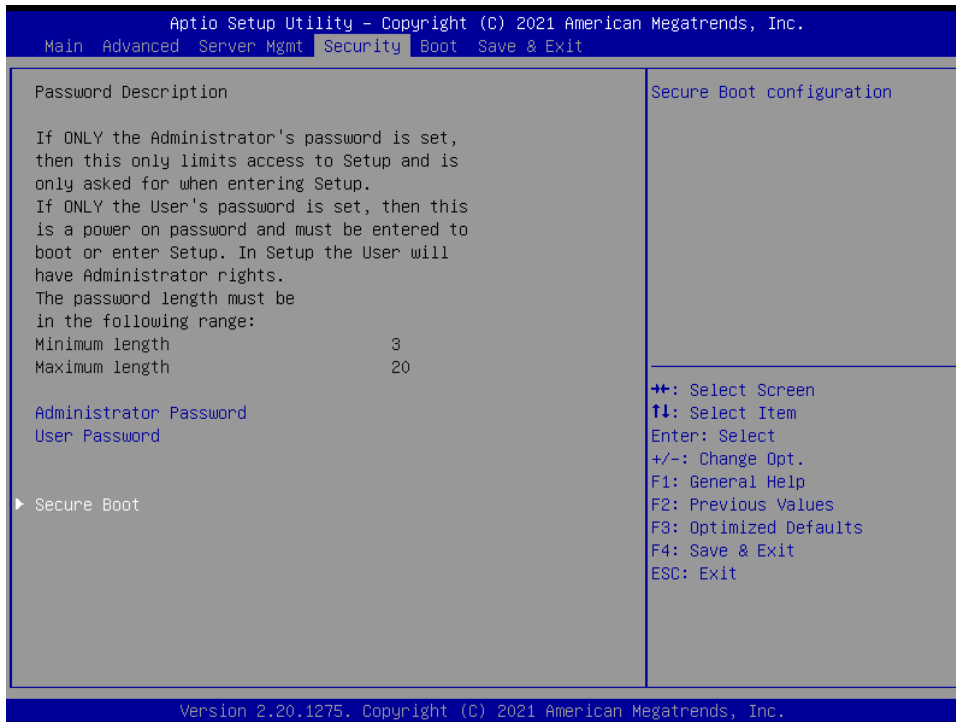


| Item | Option | Description |
|-------------------------------------|---|--|
| Configuration Address source | Unspecified[Default] Static DynamicBmcDhcp DynamicBmcNonDhcp | Select configure LAN channel parameters statically or dynamically(by BIOS or BMC). Unspecified option will not modify any BMC network parameters during BIOS phase. |
| IPV6 Support | Enabled[Default] Disabled | Enable or Disable LAN1 IPv6 Support. |
| Configuration Address source | Unspecified[Default] Static DynamicBmcDhcp | Select to configure LAN channel parameters statically or dynamically(by BIOS or BMC). Unspecified option will not modify any BMC network parameters during BIOS phase. |

3.6.3.4 BMC User Settings



3.6.4 Security



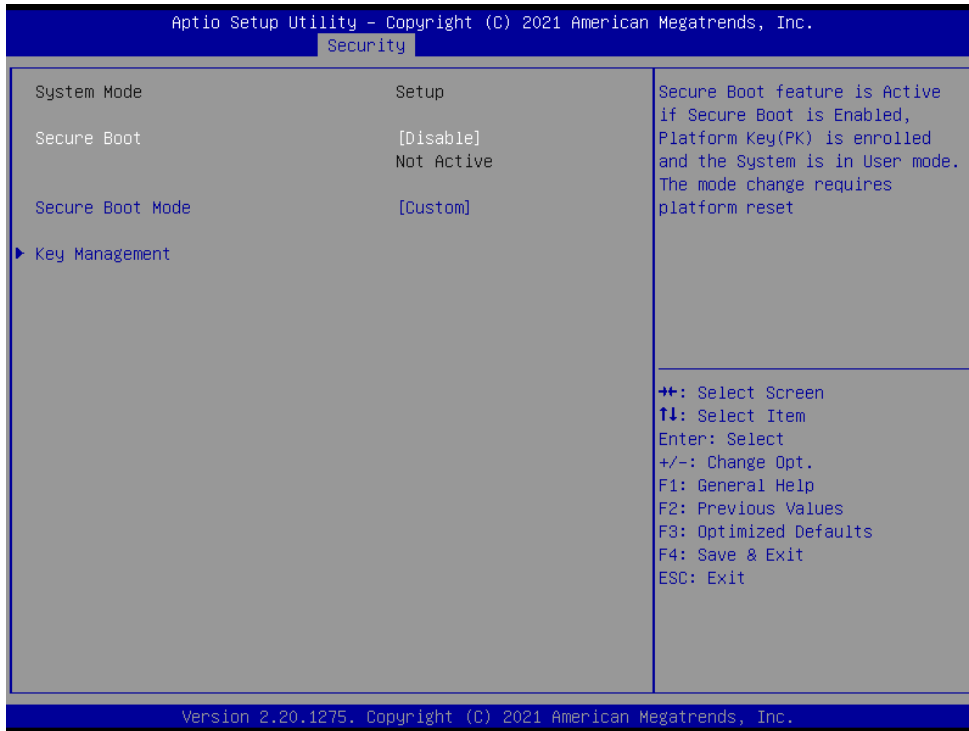
- **Administrator Password**

Set setup Administrator Password

- **User Password**

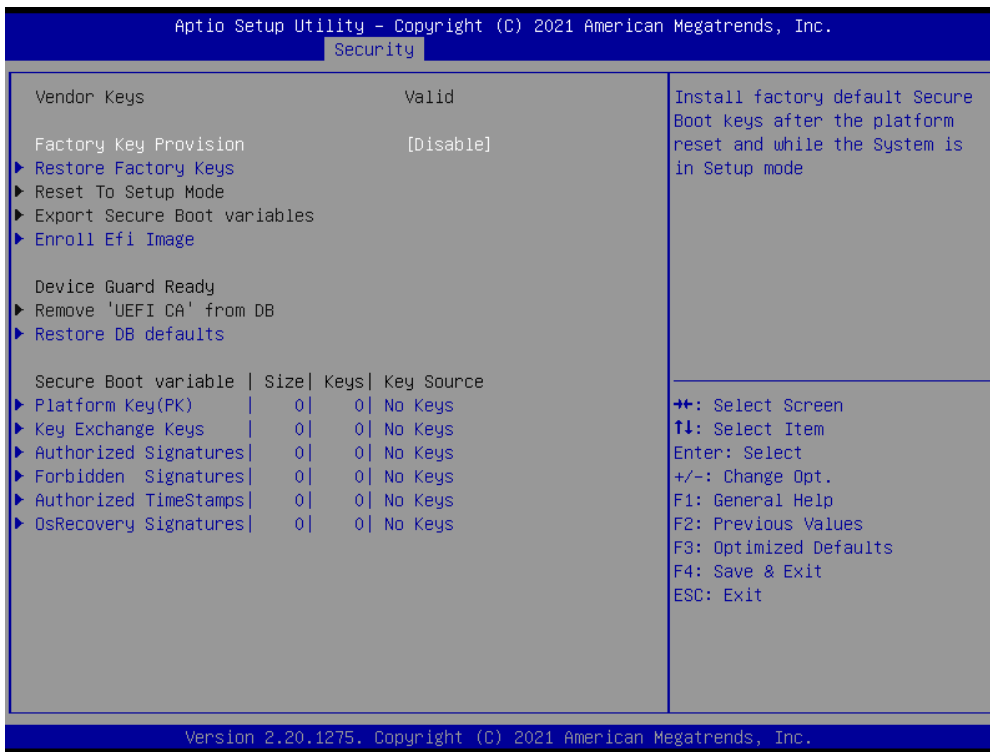
Set User Password

3.6.4.1 Secure Boot



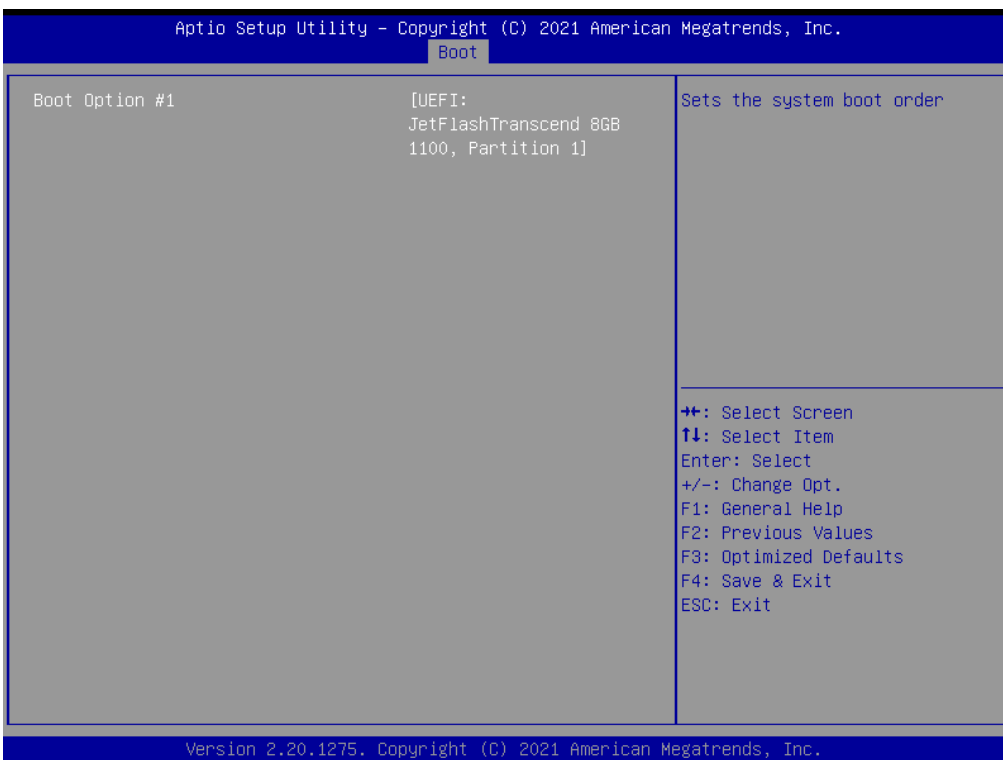
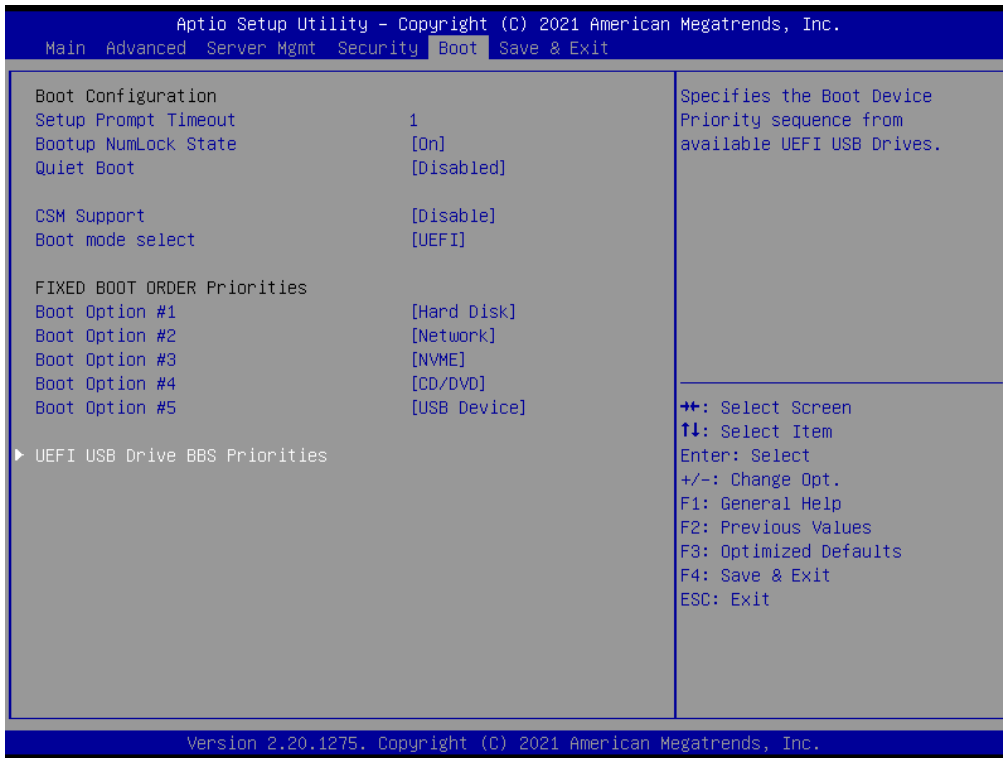
| Item | Option | Description |
|-------------------------|-----------------------------|---|
| Secure Boot | Disable[Default] Enable | Secure Boot feature is Active if Secure Boot is Enable, Platform Key(PK) is enrolled and the System is in User mode. The mode change requires platform reset. |
| Secure Boot Mode | Standard Custom[Default] | Secure Boot mode selector: Standard/Custom. In Custom mode Secure Boot Variables can be configured without authentication. |

3.6.4.1.1 Key Management



| Item | Option | Description |
|------------------------------|----------------------------|--|
| Factory Key Provision | Disable[Default] Enable | Install factory default Secure Boot keys after the platform reset and while the System is in Setup mode. |

3.6.5 Boot

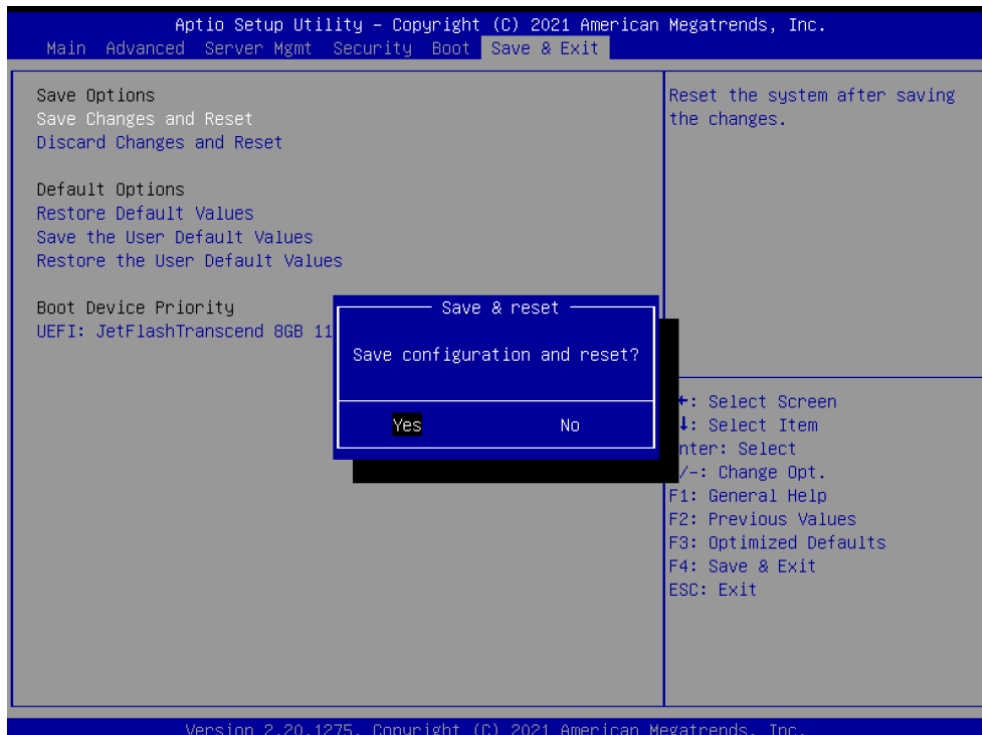
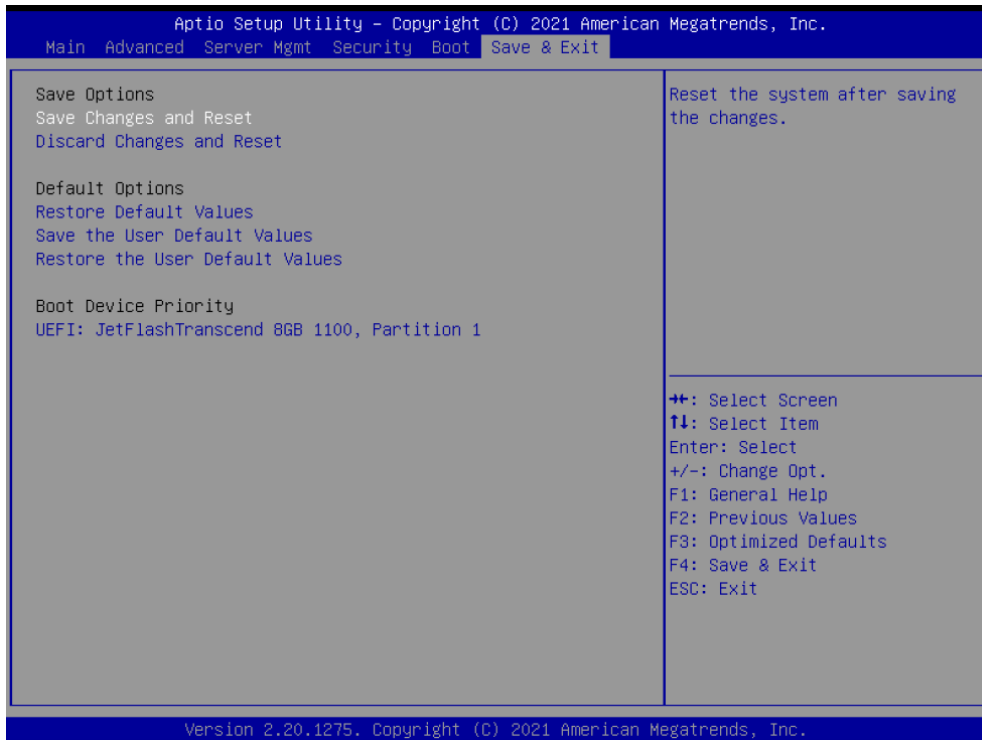


| Item | Option | Description |
|----------------------|----------|---|
| Setup Prompt Timeout | 1~ 65535 | Set the default timeout before system boot. A value of 65535 will disable the timeout completely. |

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| | | |
|-----------------------------------|---------------------------------------|---------------------------------------|
| Bootup NumLock State | On[Default] Off | Select the keyboard NumLock state |
| Quiet Boot | Disabled[Default] Enabled | Enables or disables Quiet Boot option |
| CSM Support | Disabled[Default] Enabled | Enable/Disable CSM Support. |
| Boot mode select | LEGACY UEFI[Default] | Select boot mode LEGACY/UEFI. |
| Boot Option #1/#2/#3/#4/#5 | Set the system boot order. | |

3.6.6 Save and exit



3.6.6.1 Save Changes and Reset

Reset the system after saving the changes.

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3.6.6.2 *Discard Changes and Reset*

Reset system setup without saving any changes.

3.6.6.3 *Restore Default Values*

This option restores all BIOS settings to the factory default. This option is useful if the controller exhibits unpredictable behavior due to an incorrect or inappropriate BIOS setting.

3.6.6.4 *Save the User Default Values*

Restore/Load Default values for all the setup options.

3.6.6.5 *Restore the User Default Values*

Restore the User Defaults to all the setup options.

4. Drivers Installation



Note: Installation procedures and screen shots in this section are for your reference and may not be exactly the same as shown on your screen.

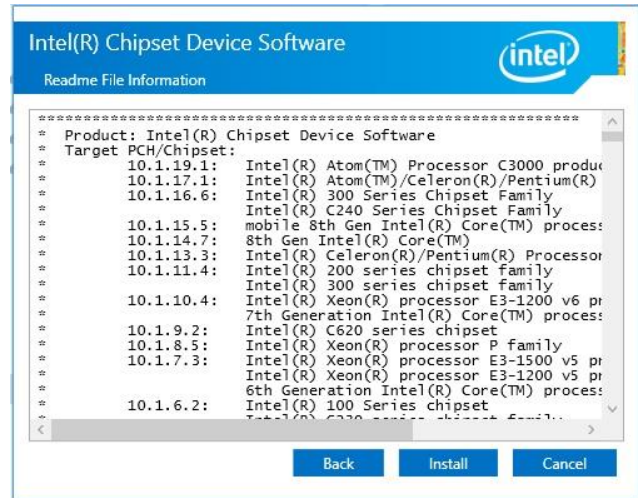
4.1 Install Chipset Driver

All drivers can be found on the Avalue Official Website:

<http://www.avalue.com.tw>.



Note: The installation procedures and screen shots in this section are based on Windows 10 operation system. If the warning message appears while the installation process, click Continue to go on.



Step 3. Click Install.



Step1. Click Next.



Step 4. Setup completed.



Step 2. Click Accept.

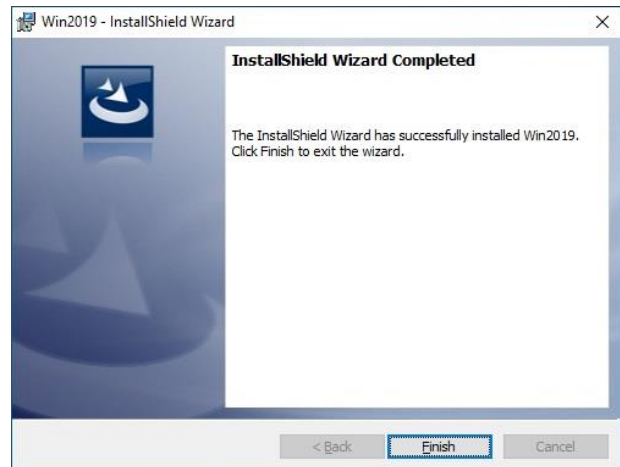
4.2 Install VGA Driver

All drivers can be found on the Avalue Official Website:

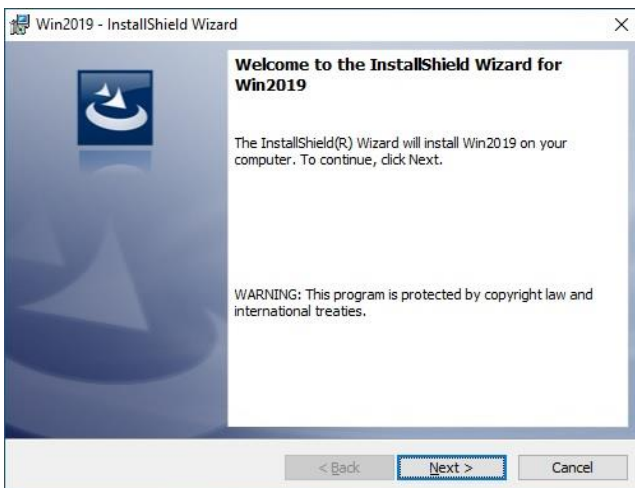
<http://www.avalu.com.tw>.



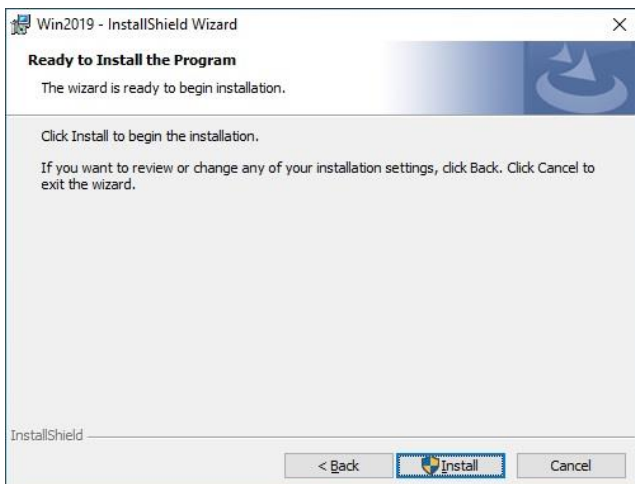
Note: The installation procedures and screen shots in this section are based on Windows 10 operation system.



Step 3. Click **Finish** to complete setup.



Step 1. Click **Next** to continue installation.



Step 2. Click **Install**.

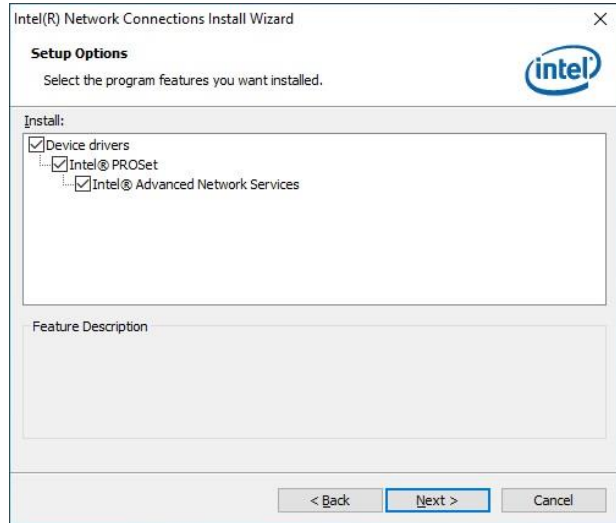
4.3 Install Ethernet Driver

All drivers can be found on the Avalue Official Website:

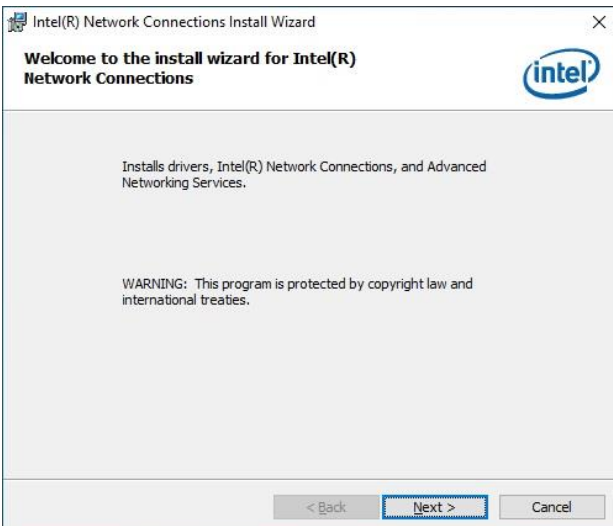
<http://www.avalue.com.tw>.



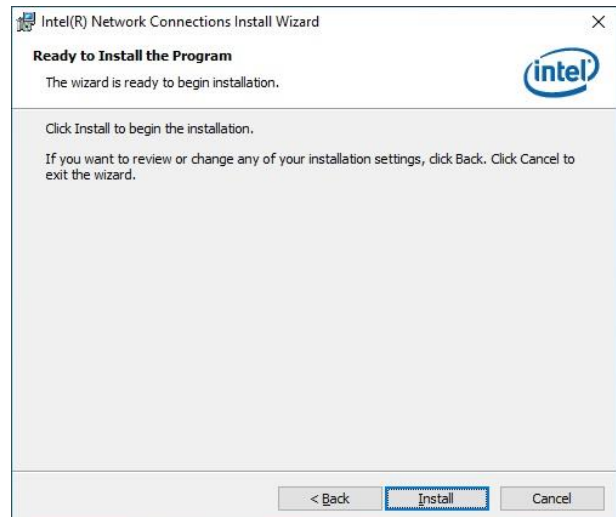
Note: The installation procedures and screen shots in this section are based on Windows 10 operation system.



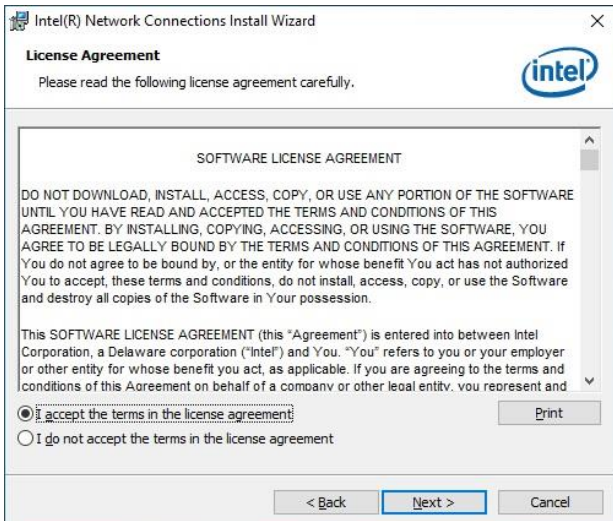
Step 3. Click Next.



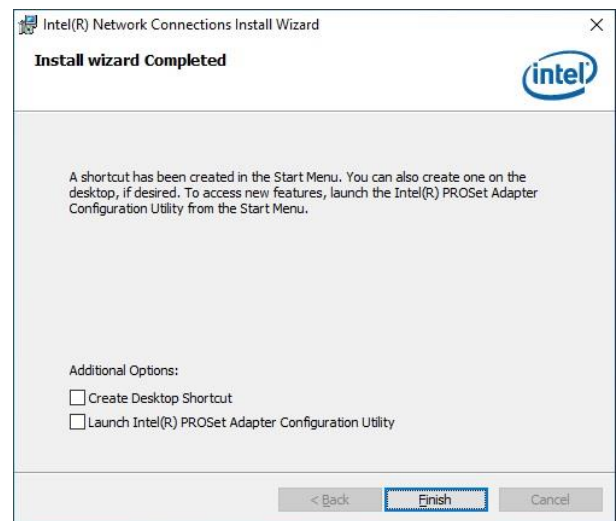
Step 1. Click Next to continue installation.



Step 4. Click Install.



Step 2. Click Next.



Step 5. Click Finish to complete setup.

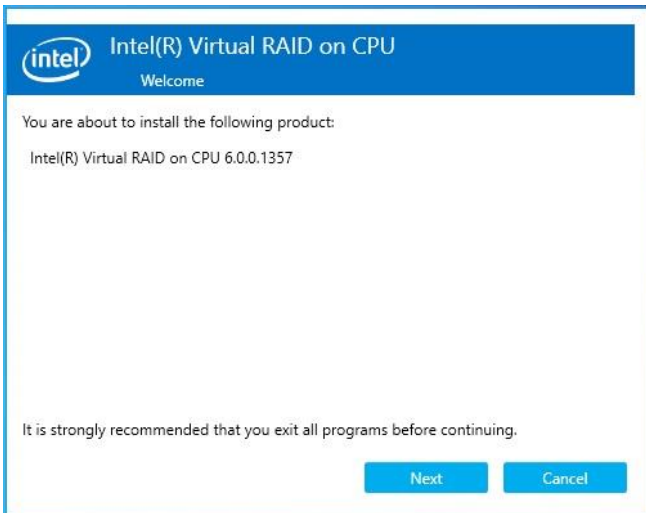
4.4 Install VROC Driver

All drivers can be found on the Avalue Official Website:

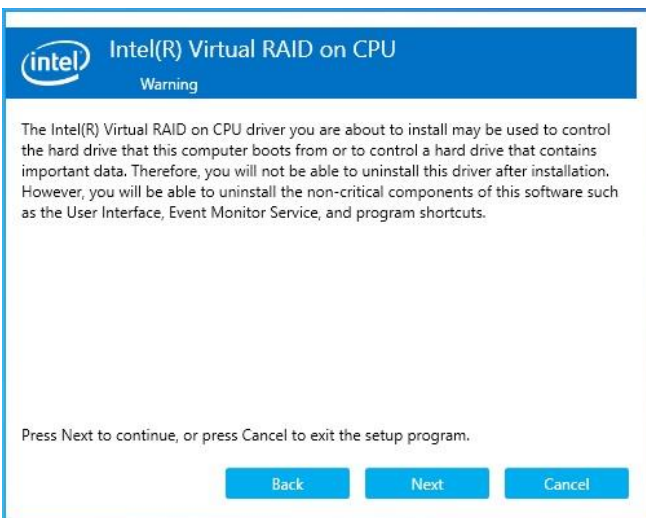
<http://www.avalue.com.tw>.



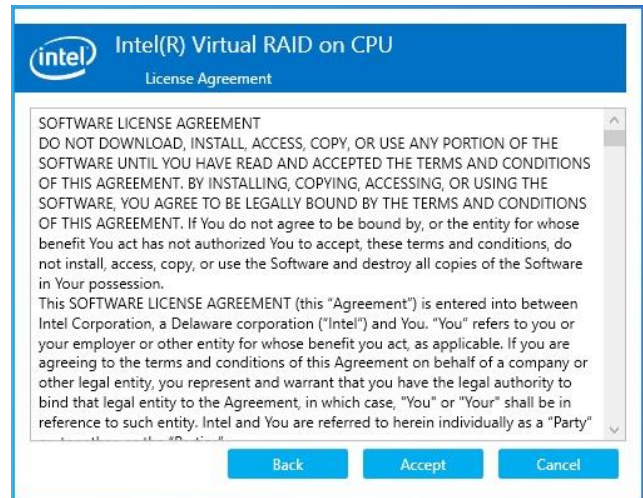
Note: The installation procedures and screen shots in this section are based on Windows 10 operation system.



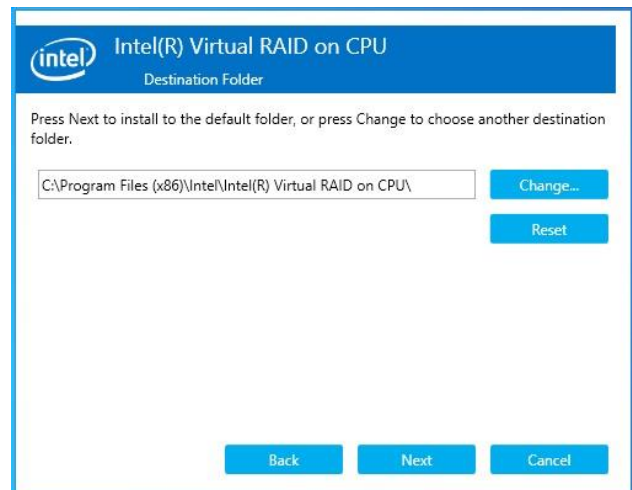
Step 1. Click **Next** to continue installation.



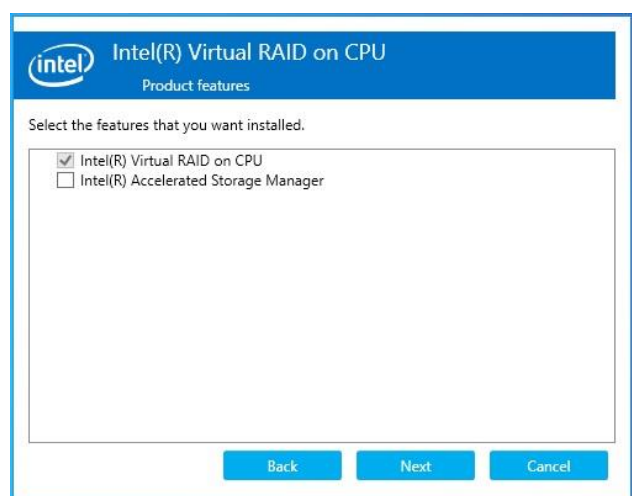
Step 2. Click **Next**.



Step 3. Click **Accept**.

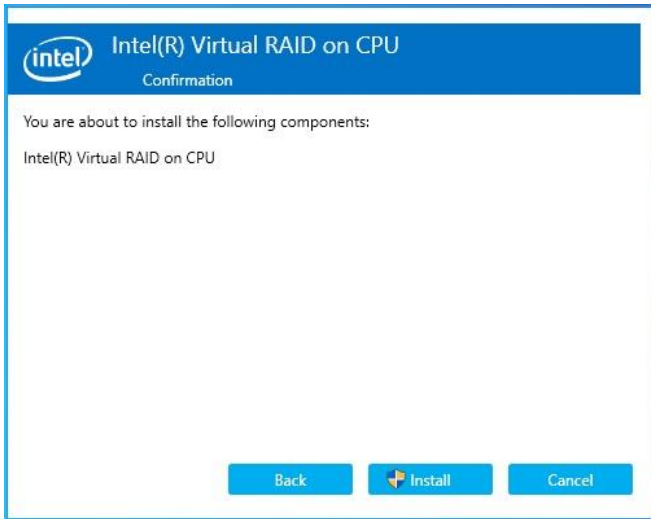


Step 4. Click **Next**.

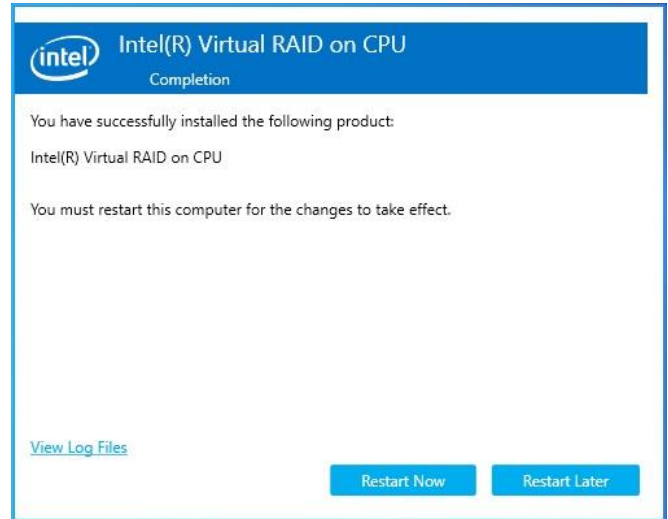


Step 5. Click **Next**.

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Step 6. Click **Install** to complete setup.



Step 7. Setup completed.

